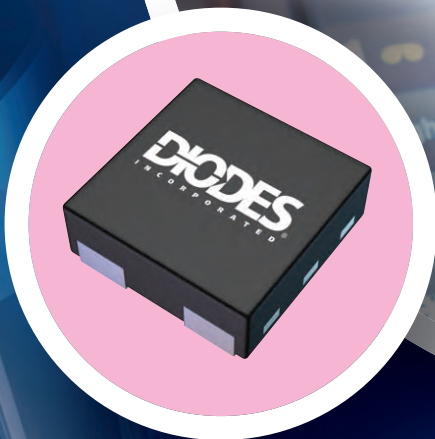


LOGIC PRODUCTS



COMPANY OVERVIEW

Diodes Incorporated is a leading global provider of Discrete, Analog, and Logic semiconductors.

Its global footprint includes sales offices in six countries and manufacturing locations in China, Europe and the USA.

A focus on product innovation, cost reduction, acquisitions and customer service has made Diodes Incorporated an industry leader.



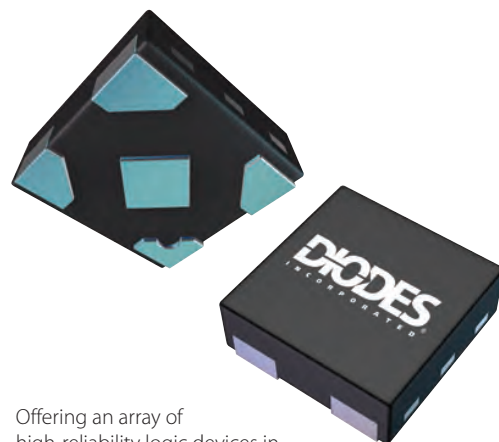
Diodes' products include diodes, rectifiers, transistors, MOSFETs, protection devices, functional specific arrays, five logic families, amplifiers and comparators, Hall-effect and temperature sensors; power management devices, including LED drivers, AC-DC converters and controllers, DC-DC switching and linear voltage regulators, and voltage references along with special function devices, such as USB power switches, load switches, voltage supervisors, and motor controllers.

DIODES' LOGIC DEVICES

Diodes Incorporated's diverse range of logic products are offered in a wide variety of packages to meet the design needs of both cutting-edge and legacy products.

Diodes Incorporated's experienced design team has created popular functions in four major logic families. Very low power AUP products are available to save milliwatts and extend battery life. The LVC family is an industry standard that has the drive and speed to work well in many demanding applications. HC and AHC legacy parts have been re-engineered on advanced wafer processes allowing for a continuous product supply well into the future.

Single-gate and dual-gate logic is widely used in many systems to interface larger more complex devices. System designs dictate that signals may need to be combined, gated, inverted or level shifted between devices. Designers now have the flexibility to add the exact number of gates in very specific locations, thus easing routing and maximizing efficiency, whereas previously a NAND gate would have required a 14-pin device.



Offering an array of high-reliability logic devices in miniature packages is crucial in order to meet the small-footprint requirements for the ever-evolving tablet, notebook and smart phone markets.

Diodes Incorporated provides an assortment of small package options, allowing manufacturers to choose a size and lead pitch that will be compatible with their standard manufacturing processes. Most of the logic products are assembled in Diodes Incorporated's proprietary factories, which enables direct control of quality and cost along with dedicated capacity to meet customer needs. While logic is often considered a commodity device, the newest miniature package offerings from Diodes Incorporated are enabling the advancement of the next generation of tiny portable consumer products.



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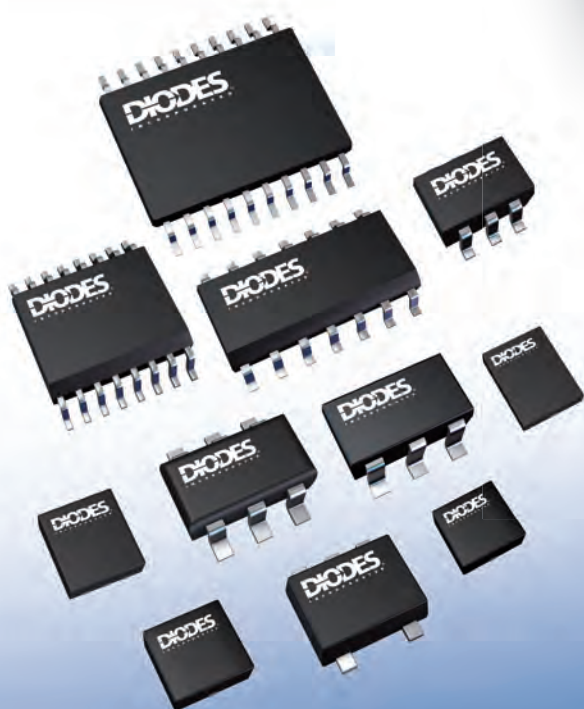
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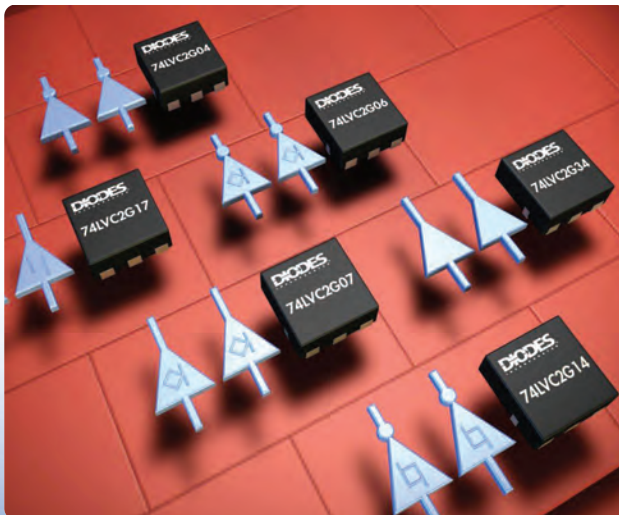
LVC

Single and Dual Gate Low Voltage CMOS Logic

Part Number	Description	Family	V _{CC} (min)	V _{CC} (max)	t _{pd} max at 1.8V	t _{pd} max at 2.5V	t _{pd} max at 3.3V	t _{pd} max at 5.0V	Output Current	Package / Package Code									
			V	V	(ns)	(ns)	(ns)	(ns)	(mA)	SOT25 / W5	SOT26 / W6	SOT353 / SE	SOT363 / DW	SOT553 / W	SOT563 / W6	DFN0808 / FS3	DFN1010 / FW4	DFN1409 / FX4	DFN1410 / FZ4
74LVC1G00	Single 2-Input NAND Gate	LVC	1.65	5.5	9	5.5	4.7	4	32	•		•		•	†	•		•	
74LVC1G02	Single 2-Input NOR Gate	LVC	1.65	5.5	8	5.5	4.5	4	32	•		•		•	†	•		•	
74LVC1G04	Single Inverter	LVC	1.65	5.5	7.5	5.2	4.2	3.7	32	•		•		•	†	•		•	
74LVC1G06	Single Inverter with Open-Drain Output	LVC	1.65	5.5	6.5	4	4	3	32	•		•		•	†	•		•	
74LVC1G07	Single Buffer with Open-Drain Output	LVC	1.65	5.5	8.3	5.5	4.2	3.5	32	•		•		•	†	•		•	
74LVC1G08	Single 2-Input AND Gate	LVC	1.65	5.5	8	5.5	4.5	4	32	•		•		•	†	•		•	
74LVC1G10	Single 3-Input NAND Gate	LVC	1.65	5.5	19	6.5	5	3.6	32		•		•			•		•	
74LVC1G11	Single 3-Input AND Gate	LVC	1.65	5.5	17.2	6.2	4.9	3.5	32		•		•			•		•	
74LVC1G14	Single Schmitt Trigger Inverter	LVC	1.65	5.5	9.9	5.5	4.6	4.4	32	•		•			†	•		•	
74LVC1G17	Single Schmitt Trigger Buffer	LVC	1.65	5.5	11	6.5	5.5	5	32	•		•			†	•		•	
74LVC1G32	Single 2-Input OR Gate	LVC	1.65	5.5	8	5.5	4.5	4	32	•		•		•	†	•		•	
74LVC1G34	Single Buffer	LVC	1.65	5.5	7.5	5	4.2	3.7	32						•	•		•	
74LVC1G57	Configurable Multiple-Function Gate	LVC	1.65	5.5	14.4	8.3	6.3	5.1	32		•		•			•		•	
74LVC1G58	Configurable Multiple-Function Gate	LVC	1.65	5.5	14.4	8.3	6.3	5.1	32		•		•			•		•	
74LVC1G86	Single 2-Input EXCLUSIVE OR Gate	LVC	1.65	5.5	9.9	5.5	5	4	32	•		•		•	†	•		•	
74LVC1G97	Configurable Multiple-Function Gate	LVC	1.65	5.5	14.4	8.3	6.3	5.1	32		•		•			•		•	
74LVC1G98	Configurable Multiple-Function Gate	LVC	1.65	5.5	14.4	8.3	6.3	5.1	32		•		•			•		•	
74LVC1G125	Single Buffer with 3-State Output OE LOW	LVC	1.65	5.5	9	5.5	4.5	4	32	•		•		•	†	•		•	
74LVC1G126	Single Buffer with 3-State Output OE HIGH	LVC	1.65	5.5	8	5.5	4.5	4	32	•		•		•	†	•		•	
74LVC1T45	Single Translator Dual Voltage	LVC	1.65	5.5	17.7	8.5	5.8	3.9	32		†		†	†		•	•	•	
74LVC2G04	Dual Inverters	LVC	1.65	5.5	8	4.4	4.1	3.2	32							•	†	•	
74LVC2G06	Dual Inverters with Open-Drain Outputs	LVC	1.65	5.5	6.5	3.9	3.4	2.9	32		•		•			•	†	•	
74LVC2G07	Dual Buffers with Open-Drain Outputs	LVC	1.65	5.5	6.7	4.3	3.7	2.9	32		•		•			•	†	•	
74LVC2G14	Dual Schmitt Trigger Inverters	LVC	1.65	5.5	11	6.5	6	4.3	32		•		•			•	†	•	
74LVC2G17	Dual Schmitt Trigger Buffers	LVC	1.65	5.5	10.5	6.5	5.7	4.3	32		•		•			•	†	•	
74LVC2G34	Dual Buffers	LVC	1.65	5.5	8.6	4.4	4.1	3.2	32		•		•			•	†	•	

† product in development

THE DIODES ADVANTAGE



The Diodes Advantage

- New DFN0808 Package**
 One of the world's smallest logic packages at 0.8 mm X 0.8 mm.
- Your Product can now be Smaller, Lighter, and Thinner**
 Today's leading products are small and thin. The DFN1010 package will allow the use of 74LVC1Gxx and 74LVC2Gxx logic in very small products including tablet computers and touch screen phones.
- Configurable Gate Functions and Dual Gate**
 Diodes offers both Dual Gate and Multifunction Gates in this very small package. Few other logic vendors have this offering.
- Chip Scale Alternative**
 The footprint is similar to a chip scale package. The DFN1409 is a good solution for products that can accommodate 0.4 mm height. The packaged part uses less silicon and is more economical than a chip scale solution.



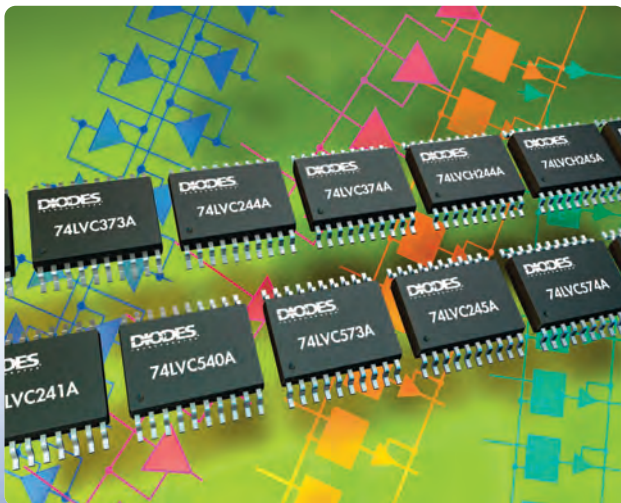
LVC

Standard Low Voltage CMOS Logic

Part Number	Description	Family	V _{CC} (min)	V _{CC} (max)	t _{pd} max at 1.8V	t _{pd} max at 2.5V	t _{pd} max at 3.3V	Output Current (mA)	Package / Package Code		
			V	V	(ns)	(ns)	(ns)		SO-14 / S14	TSSOP-14 / T14	QFN-20 / Q20
74LVC00A	Quadruple 2-Input NAND Gates	LVC	1.65	3.6	12	5.9	4.1	24	•	•	
74LVC04A	Hex Buffers	LVC	1.65	3.6	75	7	4.3	24	•	•	
74LVC06A	Hex Inverters with Open-Drain Outputs	LVC	1.65	3.6	5.3	4.1	3.5	24	•	•	
74LVC07A	Hex Buffers with Open-Drain Outputs	LVC	1.65	3.6	5.3	4.1	3.5	24			
74LVC08A	Quadruple 2-Input AND Gates	LVC	1.65	3.6	9.3	6.4	3.9	24	•	•	
74LVC14A	Hex Schmitt Trigger Inverters	LVC	1.65	3.6	8.2	7	4.3	24	•	•	
74LVC32A	Quadruple 2-Input OR Gates	LVC	1.65	3.6	8.2	4.9	3.6	24	•	•	
74LVC86A	Quadruple 2-Input EXCLUSIVE OR Gates	LVC	1.65	3.6	9.4	7.1	4.4	24	•	•	
74LVC125A	Quadruple 3-State Buffers OE LOW	LVC	1.65	3.6	11.8	5.8	4.6	24	•	•	
74LVC126A	Quadruple 3-State Buffers OE HIGH	LVC	1.65	3.6	9.3	6.7	4.5	24	•	•	
74LVC240A	Octal Buffers/Line Drivers with 3-State Outputs	LVC	1.65	3.6	12.7	8.3	6.3	24			†
74LVC241A	Octal Buffers/Line Drivers with 3-State Outputs	LVC	1.65	3.6	12.7	8.3	6.1	24			†
74LVC244A	Octal Buffers/Line Drivers with 3-State Outputs	LVC	1.65	3.6	10.9	7.9	6.9	24			†
74LVC245A	Octal Bus Transceivers with 3-State Outputs	LVC	1.65	3.6	12.7	8.3	7.3	24			†
74LVC273A	Octal D-Type Flip-Flops with Clear	LVC	1.65	3.6	16.5	9	7.3	24			†
74LVC373A	Octal Transparent D-Type Latches with 3-State Outputs	LVC	1.65	3.6	12.2	7.8	6.8	24			†
74LVC374A	Octal D-Type Flip-Flops with 3-State Outputs	LVC	1.65	3.6	12.2	8.5	7.8	24			†
74LVC540A	Octal Buffers/Line Drivers with 3-State Outputs	LVC	1.65	3.6	12.2	7.6	6.9	24			†
74LVC541A	Octal Buffers/Line Drivers with 3-State Outputs	LVC	1.65	3.6	13.2	7.6	4.9	24			†
74LVC573A	Octal Transparent D-Type Latches with 3-State Outputs	LVC	1.65	3.6	15.3	10.5	9.5	24			†
74LVC574A	Octal D-Type Flip-Flops with 3-State Outputs	LVC	1.65	3.6	12.2	8.5	6.8	24			†
74LVCH244A	Octal Buffers/Line Drivers with Bus Hold / 3-State Outputs	LVC	1.65	3.6	10.9	7.9	5.9	24			†
74LVCH245A	Octal Bus Transceivers with Bus Hold / 3-State Outputs	LVC	1.65	3.6	12.7	8.3	6.3	24			†

† product in development

THE DIODES ADVANTAGE



The Diodes Advantage

- Wide Supply Voltage Range**
 The recommended operating voltage range is V_{CC} from 1.65 to 3.6 V.
- I_{off} Circuit Included**
 Diodes offers the designer the flexibility of power down isolation for sections of a system. I_{off} circuits are not offered by all vendors for LVC products.
- Direct Replacement**
 The LVCxx series offers a direct replacement for the industry standard LVC providing better value and improved availability.



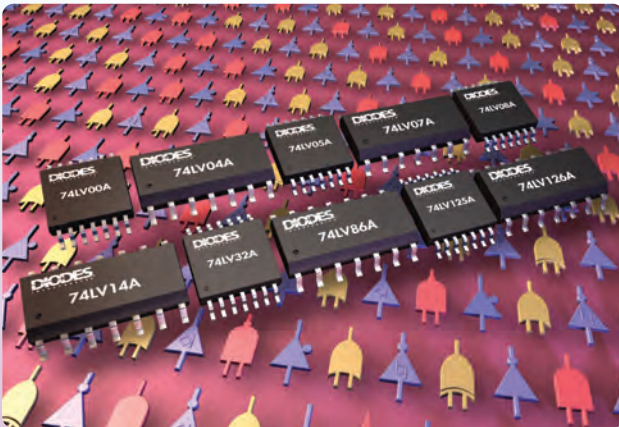
LV

Low Voltage CMOS

Part Number	Description	Family	V _{CC} (min)	V _{CC} (max)	t _{pd} max at 2.5V	t _{pd} max at 3.3V	t _{pd} max at 5.0V	Output Current (mA)	Package / Package Code		
			V	V	(ns)	(ns)	(ns)		SO-14 / S14	TSSOP-14 / T14	QFN-20 / Q20
74LV00A	Quadruple 2-Input NAND Gates	LV	2.0	5.5	15	9.5	6.5	12	†	†	
74LV04A	Hex Buffers	LV	2.0	5.5	14	8.5	6.5	12	†	†	
74LV05A	Hex Inverters with Open-Drain Outputs	LV	2.0	5.5	15	8.5	6.5	12	†	†	
74LV06A	Hex Inverters with Open-Drain Outputs	LV	2.0	5.5	15	8.5	6.5	12	†	†	
74LV07A	Hex Buffers with Open-Drain Outputs	LV	2.0	5.5	15	8	6	12	†	†	
74LV08A	Quadruple 2-Input AND Gates	LV	2.0	5.5	16	10.5	7	12	†	†	
74LV14A	Hex Schmitt Trigger Inverters	LV	2.0	5.5	22	15	10	12	†	†	
74LV32A	Quadruple 2-Input OR Gates	LV	2.0	5.5	15	9.5	6.5	12	†	†	
74LV86A	Quadruple 2-Input EXCLUSIVE OR Gates	LV	2.0	5.5	21	13	8	12	†	†	
74LV125A	Quadruple 3-State Buffers OE LOW	LV	2.0	5.5	15.5	9.5	6.5	12	†	†	
74LV126A	Quadruple 3-State Buffers OE HIGH	LV	2.0	5.5	15.5	9.5	6.5	12	†	†	
74LV240A	Octal Buffers/Line Drivers with 3-State Outputs	LV	2.0	5.5	14	9	6.5	12			†
74LV241A	Octal Buffers/Line Drivers with 3-State Outputs	LV	2.0	5.5	15	10	6.5	12			†
74LV244A	Octal Buffers/Line Drivers with 3-State Outputs	LV	2.0	5.5	15	10	6.5	12			†
74LV245A	Octal Bus Transceivers with 3-State Outputs	LV	2.0	5.5	15	10	6.5	12			†
74LV273A	Octal D-Type Flip-Flops with Clear	LV	2.0	5.5	20.5	16	10.5	12			†
74LV373A	Octal Transparent D-Type Latches with 3-State Outputs	LV	2.0	5.5	17	13.5	8.5	12			†
74LV374A	Octal D-Type Flip-Flops with 3-State Outputs	LV	2.0	5.5	19	15	9.5	12			†
74LV540A	Octal Buffers/Line Drivers with 3-State Outputs	LV	2.0	5.5	14	8.5	6	12			†
74LV541A	Octal Buffers/Line Drivers with 3-State Outputs	LV	2.0	5.5	13.5	8.5	6	12			†
74LV573A	Octal Transparent D-Type Latches with 3-State Outputs	LV	2.0	5.5	18	13	8	12			†
74LV574A	Octal D-Type Flip-Flops with 3-State Outputs	LV	2.0	5.5	20	15.5	10	12			†

† product in development

THE DIODES ADVANTAGE



The Diodes Advantage

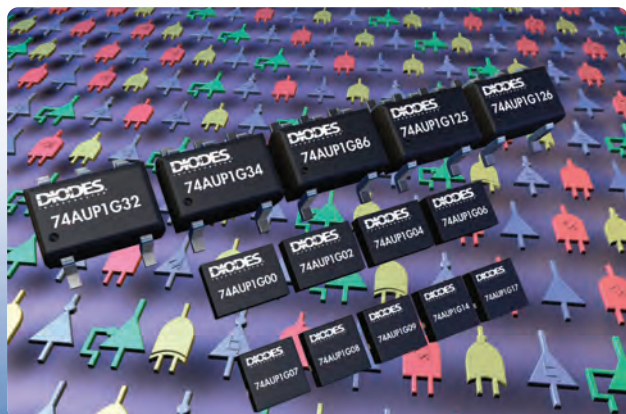
- Wide Supply Voltage Range**
 The recommended operating voltage range is V_{CC} from 2.0 to 5.5V. This allows a wide range of applications including 5V legacy systems.
- Upgrade Path**
 Diodes offers the designer the flexibility of a system upgrade from HC or AHC technologies. The compatible voltage levels and faster switching speeds allow for faster clock rates in legacy systems.
- Direct Replacement**
 The LVxx series offers a direct replacement for the industry standard LV products.

AUP

Single and Dual Gate Advanced Ultra Low Power CMOS Logic

Part Number	Description	Family	V _{CC} (min)	V _{CC} (max)	t _{pd} max at 1.5V	t _{pd} max at 1.8V	t _{pd} max at 2.5V	t _{pd} max at 3.3V	Output Current	Package / Package Code							
			V	V	(ns)	(ns)	(ns)	(ns)	(mA)	SOT353 / SE	SOT363 / DW	DFN0808 / FS3	DFN0910 / FW3	DFN1010 / FW4	DFN1410 / FW4	DFN1210 / RA3	DFN2030 / RB5
74AUP1G00	Single 2-Input NAND Gate	AUP	0.8	3.6	8.9	7	5.3	4.9	4	•		†		•	•		
74AUP1G02	Single 2-Input NOR Gate	AUP	0.8	3.6	8.9	7	5.4	4.8	4	•		†		•	•		
74AUP1G04	Single Inverter	AUP	0.8	3.6	9	6.7	5.1	4.2	4	•		†		•	•		
74AUP1G06	Single Inverter with Open-Drain Output	AUP	0.8	3.6	7.7	6.6	4.6	6	4	•		†		•	•		
74AUP1G07	Single Buffer with Open-Drain Output	AUP	0.8	3.6	6.8	6.7	5.9	5.7	4	•		†		•	•		
74AUP1G08	Single 2-Input AND Gate	AUP	0.8	3.6	8.6	6.9	5.5	4.8	4	•		†		•	•		
74AUP1G09	Single 2-Input AND Gate width Open-Drain Output	AUP	0.8	3.6	12.1	9.6	8.1	5.5	4	•		†		•	•		
74AUP1G14	Single Schmitt Trigger Inverter	AUP	0.8	3.6	8.6	7	5.5	4.8	4	•		†		•	•		
74AUP1G17	Single Schmitt Trigger Buffer	AUP	0.8	3.6	8.7	7.1	5.6	4.9	4	•		†		•	•		
74AUP1G32	Single 2-Input OR Gate	AUP	0.8	3.6	8.6	6.7	5.3	4.9	4	•		†		•	•		
74AUP1G34	Single Buffer	AUP	0.8	3.6	7.6	6	4.8	4.2	4	•		†		•	•		
74AUP1G86	Single 2-Input EXCLUSIVE OR Gate	AUP	0.8	3.6	12.5	9.8	7.6	7.1	4	•		†		•	•		
74AUP1G125	Single Buffer with 3-State Output OE LOW	AUP	0.8	3.6	8.1	6.3	4.9	4.4	4	•		†		•	•		
74AUP1G126	Single Buffer with 3-State Output OE HIGH	AUP	0.8	3.6	8.1	6.3	4.9	4.4	4	•		†		•	•		
74AUP2G00	Dual 2-Input NAND Gates	AUP	0.8	3.6	12.9	4	5.3	4.9	4					†		†	†
74AUP2G02	Dual 2-Input NOR Gates	AUP	0.8	3.6	10.9	8	5.5	4.8	4					†		†	†
74AUP2G04	Dual Inverters	AUP	0.8	3.6	11	7.7	5.6	4.2	4		•		†				
74AUP2G06	Dual Inverters with Open-Drain Outputs	AUP	0.8	3.6	10.7	7.6	5.1	6	4		•		†				
74AUP2G07	Dual Buffers with Open-Drain Outputs	AUP	0.8	3.6	9.8	8.7	6.9	5.7	4		•		†				
74AUP2G08	Dual 2-Input AND Gates	AUP	0.8	3.6	11.6	8.9	6.5	4.8	4					†		†	†
74AUP2G14	Dual Schmitt Trigger Inverters	AUP	0.8	3.6	11.6	9	6.5	4.8	4		•		†				
74AUP2G17	Dual Schmitt Trigger Buffers	AUP	0.8	3.6	11.7	9.1	6.6	4.9	4		•		†				
74AUP2G32	Dual 2-Input OR Gates	AUP	0.8	3.6	11.6	8.7	6.3	4.9	4					†		†	†
74AUP2G34	Dual Buffers	AUP	0.8	3.6	10.6	8	5.8	4.2	4		•		†				
74AUP2G86	Dual 2-Input EXCLUSIVE OR Gate	AUP	0.8	3.6	15.5	11.8	8.6	7.1	4					†		†	†
74AUP2G125	Dual Buffers with 3-State Outputs OE LOW	AUP	0.8	3.6	11.1	8.3	6.9	4.4	4					†		†	†
74AUP2G126	Dual Buffers with 3-State Outputs OE HIGH	AUP	0.8	3.6	11.9	8.8	7.1	4.7	4					†		†	†
74AUP2G3404	Dual - One Buffer & One Inverter	AUP	0.8	3.6	11.8	7.7	6.2	4.6	4				•				

† product in development



The Diodes Advantage

Low Voltage and Low Power

Parts are well suited for battery driven, handheld applications such as cell phones, tablets, E-readers, games, cameras, music players, netbooks, and notebooks.

Noise Rejection Circuitry

All of the devices in this release include a small amount of input hysteresis making less susceptible to problems from slow rising or falling signals.

Small Packages

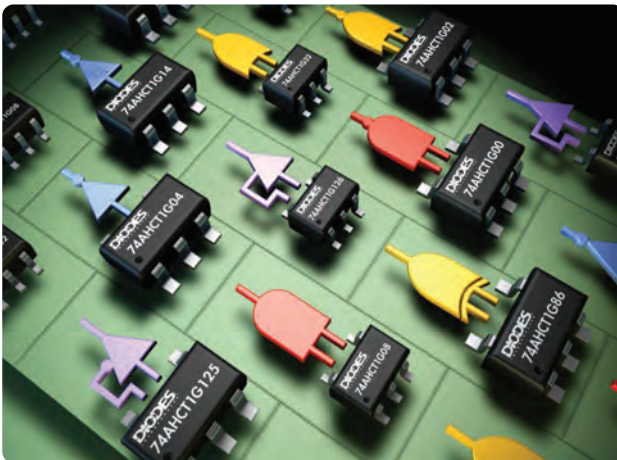
The DFN0808 at 0.8 mm X 0.8 mm and the DFN1010 at 1.0 X 1.0 mm are ideal for the smallest of applications.

AHC/AHCT

Single Gate Advanced High Speed CMOS Logic

Part Number	Description	Family	V _{CC} (min)	V _{CC} (max)	t _{pd} max at 3.3V	t _{pd} max at 5.0V	Output Current (mA)	Package / Package Code	
			V	V	(ns)	(ns)		SOT25 / W5	SOT353 / SE
74AHC1G00	Single 2-Input NAND Gate	AHC	2	5.5	7.9	5.5	8	•	•
74AHC1G02	Single 2-Input NOR Gate	AHC	2	5.5	7.9	5.5	8	•	•
74AHC1G04	Single Inverter	AHC	2	5.5	7.1	5.5	8	•	•
74AHC1G08	Single 2-Input AND Gate	AHC	2	5.5	8.8	5.9	8	•	•
74AHC1G09	Single 2-Input AND Gate width Open-Drain Output	AHC	2	5.5	8.5	5.5	8	•	•
74AHC1G14	Single Schmitt Trigger Inverter	AHC	2	5.5	12.8	8.6	8	•	•
74AHC1G32	Single 2-Input OR Gate	AHC	2	5.5	7.9	5.5	8	•	•
74AHC1G86	Single 2-Input EXCLUSIVE OR Gate	AHC	2	5.5	11	6.8	8	•	•
74AHC1G125	Single Buffer with 3-State Output OE LOW	AHC	2	5.5	8	5.5	8	•	•
74AHC1G126	Single Buffer with 3-State Output OE HIGH	AHC	2	5.5	8	5.5	8	•	•
74AHC1GU04	Unbuffered Single Inverter	AHC	2	5.5	7.1	5.5	8	•	•
74AHCT1G00	Single 2-Input NAND Gate, TTL Compatible	AHCT	4.5	5.5		6.2	8	•	•
74AHCT1G02	Single 2-Input NOR Gate, TTL Compatible	AHCT	4.5	5.5		5.5	8	•	•
74AHCT1G04	Single Inverter, TTL Compatible	AHCT	4.5	5.5		6.7	8	•	•
74AHCT1G08	Single 2-Input AND Gate, TTL Compatible	AHCT	4.5	5.5		6.2	8	•	•
74AHCT1G14	Single Schmitt Trigger Inverter, TTL Compatible	AHCT	4.5	5.5		5.5	8	•	•
74AHCT1G32	Single 2-Input OR Gate, TTL Compatible	AHCT	4.5	5.5		5.5	8	•	•
74AHCT1G86	Single 2-Input EXCLUSIVE OR Gate, TTL Compatible	AHCT	4.5	5.5		7	8	•	•
74AHCT1G125	Single Buffer with 3-State Output OE LOW, TTL Compatible	AHCT	4.5	5.5		6.9	8	•	•
74AHCT1G126	Single Buffer with 3-State Output OE HIGH, TTL Compatible	AHCT	4.5	5.5		6.9	8	•	•

THE DIODES ADVANTAGE



The Diodes Advantage

■ AHC Direct Replacement

The 74AHC1Gxx is a direct replacement for devices offered by other vendors.

■ AHC Lower Power Consumption

The AHC family has drive current optimized at a lower +/- 8 mA which is the lower power alternative for 3.3V to 5.5V applications.

■ Advanced 5V Process

A new robust 5V process is an upgrade over the competition. Lower power dissipation capacitance improves dynamic switching power.

AHC/AHCT

Standard Advanced High Speed CMOS Logic

Part Number	Description	Family	V _{CC} (min)	V _{CC} (max)	t _{pd} max at 3.3V	t _{pd} max at 5.0V	Output Current (mA)	Package / Package Code			
			V	V	(ns)	(ns)		SO-14 / S14	TSSOP-14 / T14	SO-16 / S16	TSSOP-16 / T16
74AHC00	Quadruple 2-Input NAND Gates	AHC	2.0	5.5	11.4	7.5	8	•	•		
74AHC04	Hex Inverters	AHC	2.0	5.5	8.5	5.5	8	•	•		
74AHC05	Hex Inverters with Open-Drain Outputs	AHC	2.0	5.5	7.9	5.5	8	•	•		
74AHC08	Quadruple 2-Input AND Gates	AHC	2.0	5.5	7.9	5.5	8	•	•		
74AHC14	Hex Schmitt Trigger Inverters	AHC	2.0	5.5	12.8	8.6	8	•	•		
74AHC32	Quadruple 2-Input OR Gates	AHC	2.0	5.5	7.9	5.5	8	•	•		
74AHC86	Quadruple 2-Input EXCLUSIVE OR Gates	AHC	2.0	5.5	11.0	6.8	8	•	•		
74AHC125	Quadruple 3-State Buffers OE LOW	AHC	2.0	5.5	8.0	5.5	8	•	•		
74AHC126	Quadruple 3-State Buffers OE HIGH	AHC	2.0	5.5	8.0	5.5	8	•	•		
74AHC138	3-to-8 Line Decoder Demultiplexer	AHC	2.0	5.5	13	8.5	8			•	•
74AHC594	8-Bit Shift Register 8-Bit Output Register	AHC	2.0	5.5	13.2	9.1	8			•	•
74AHC595	8-Bit Shift Register 8-Bit Output Register with 3-State Outputs	AHC	2.0	5.5	18.5	11.4	8			•	•
74AHCT00	Quadruple 2-Input NAND Gates, TTL Compatible	AHCT	4.5	5.5		6.9	8	•	•		
74AHCT04	Hex Inverters, TTL Compatible	AHCT	4.5	5.5		6.7	8	•	•		
74AHCT08	Quadruple 2-Input AND Gates, TTL Compatible	AHCT	4.5	5.5		5.5	8	•	•		
74AHCT14	Hex Schmitt Trigger Inverters, TTL Compatible	AHCT	4.5	5.5		6.9	8	•	•		
74AHCT32	Quadruple 2-Input OR Gates, TTL Compatible	AHCT	4.5	5.5		6.9	8	•	•		
74AHCT86	Quadruple 2-Input EXCLUSIVE OR Gates, TTL Compatible	AHCT	4.5	5.5		6.9	8	•	•		
74AHCT125	Quadruple 3-State Buffers OE LOW, TTL Compatible	AHCT	4.5	5.5		5.5	8	•	•		
74AHCT126	Quadruple 3-State Buffers OE HIGH, TTL Compatible	AHCT	4.5	5.5		5.5	8	•	•		
74AHCT138	3-to-8 Line Decoder Demultiplexer, TTL Compatible	AHCT	4.5	5.5		10.5	8			•	•
74AHCT594	8-Bit Shift Register 8-Bit Output Register, TTL Compatible	AHCT	4.5	5.5		10.1	8			•	•
74AHCT595	8-Bit Shift Register 8-Bit Output Register with 3-State Outputs, TTL Compatible	AHCT	4.5	5.5		10.5	8			•	•
74AHC0U4	Unbuffered Hex Inverters	AHC	2.0	5.5	7.1	5.5	8	•	•		



The Diodes Advantage

■ Noise Rejection Circuitry

All of the devices in this release include a small amount of input hysteresis making less susceptible to problems from slow rising or falling signals.

■ Direct Replacement

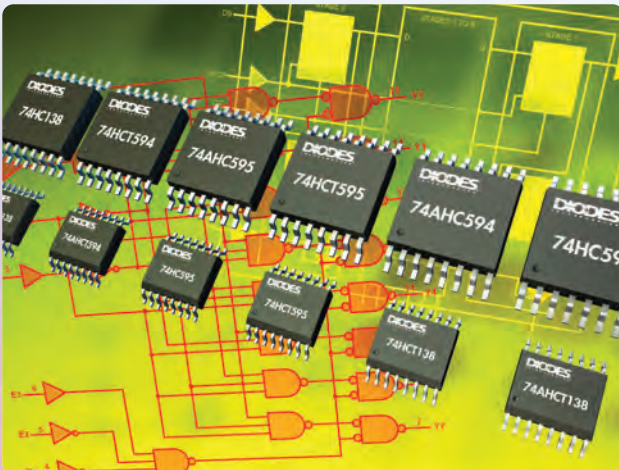
The 74AHC and AHCT devices are direct replacements for industry standard devices providing better value and improved availability.

HC/HCT

Standard High Speed CMOS Logic

Part Number	Description	Family	V_{CC} (min)	V_{CC} (max)	t_{pd} max at 2.0V	t_{pd} max at 4.5V	Output Current	Package / Package Code			
			V	V	(ns)	(ns)		SO-14 / S14	TSSOP-14 / T14	SO-16 / S16	TSSOP-16 / T16
74HC00	Quadruple 2-Input NAND Gates	HC	2.0	6.0	90	18	4	•	•		
74HC04	Hex Inverters	HC	2.0	6.0	90	18	4	•	•		
74HC05	Hex Inverters with Open-Drain Outputs	HC	2.0	6.0	90	18	4	•	•		
74HC08	Quadruple 2-Input AND Gates	HC	2.0	6.0	90	18	4	•	•		
74HC14	Hex Schmitt Trigger Inverters	HC	2.0	6.0	125	25	4	•	•		
74HC32	Quadruple 2-Input OR Gates	HC	2.0	6.0	90	18	4	•	•		
74HC86	Quadruple 2-Input EXCLUSIVE OR Gates	HC	2.0	6.0	90	18	4	•	•		
74HC125	Quadruple 3-State Buffers OE LOW	HC	2.0	6.0	100	20	4	•	•		
74HC126	Quadruple 3-State Buffers OE HIGH	HC	2.0	6.0	100	20	4	•	•		
74HC138	3-to-8 Line Decoder Demultiplexer	HC	2.0	6.0	190	38	4			•	•
74HC594	8-Bit Shift Register 8-Bit Output Register	HC	2.0	6.0	185	37	4			•	•
74HC595	8-Bit Shift Register 8-Bit Output Register with 3-State Outputs	HC	2.0	6.0	200	40	4			•	•
74HCT00	Quadruple 2-Input NAND Gates, TTL Compatible	HCT	4.5	5.5		22	4	•	•		
74HCT04	Hex Inverters, TTL Compatible	HCT	4.5	5.0		22	4	•	•		
74HCT08	Quadruple 2-Input AND Gates, TTL Compatible	HCT	4.5	5.0		24	4	•	•		
74HCT14	Hex Schmitt Trigger Inverters, TTL Compatible	HCT	4.5	5.0		34	4	•	•		
74HCT32	Quadruple 2-Input OR Gates, TTL Compatible	HCT	4.5	5.5		24	4	•	•		
74HCT86	Quadruple 2-Input EXCLUSIVE OR Gates, TTL Compatible	HCT	4.5	5.0		40	4	•	•		
74HCT125	Quadruple 3-State Buffers OE LOW, TTL Compatible	HCT	4.5	5.5		25	4	•	•		
74HCT126	Quadruple 3-State Buffers OE HIGH, TTL Compatible	HCT	4.5	5.5		25	4	•	•		
74HCT138	3-to-8 Line Decoder Demultiplexer, TTL Compatible	HCT	4.5	5.5		40	4			•	•
74HCT594	8-Bit Shift Register 8-Bit Output Register, TTL Compatible	HCT	4.5	5.5		38	4			•	•
74HCT595	8-Bit Shift Register 8-Bit Output Register with 3-State Outputs, TTL Compatible	HCT	4.5	5.5		50	4			•	•
74HC0U4	Unbuffered Hex Inverters	HC	2.0	5.5	90	18	4	•	•		

THE DIODES ADVANTAGE



The Diodes Advantage

■ Lower Power Dissipation

C_{PD} is up to 60% lower on the HCMOS parts resulting in lower power dissipation helping meet energy savings standards.

■ Noise Rejection Circuitry

All of the devices in this release include a small amount of input hysteresis making less susceptible to problems from slow rising or falling signals.

■ Direct Replacement

The 74HC and HCT devices are direct replacements for industry standard devices providing better value and improved availability.

Logic Packaging Overview

Diodes Incorporated specializes in building small packages.

With the advent of the cell phone and tablet computer the need for extremely small packages has grown exponentially.

Diodes Incorporated supplies several logic families in extremely small package formats to meet the most demanding applications.

Some of the smaller DFN packages use less board area than chip scale while offering a solution that is more robust for system drop and shear tests.

The Standard Logic portfolio is also supported by an array of standard SO and TSSOP packages and includes a small 20-pin QFN for small space-constrained systems.



ONE OF THE
WORLD'S SMALLEST
LOGIC PACKAGES
- DFN0808

DFN / QFN Package nomenclature

X2

DFN

08

08

-4

Example represents Diodes' smallest logic package.

Pin Count - Center pads not counted

Package Y Dimension in tenths of mm

Package X Dimension in tenths of mm

DFN - Leads on opposing two sides QFN - Leads on four sides

Package Thickness Range	T	> 1.00	<= 1.20
V	> 0.8	<= 1.00	
W	> 0.65	<= 0.8	
U	> 0.5	<= 0.65	
X1	> 0.4	<= 0.5	
X2	> 0.3	<= 0.4	
X3	> 0.25	<= 0.3	
X4	> 0.0	<= 0.25	

0.8mm 0.8mm

Logic Prefixes

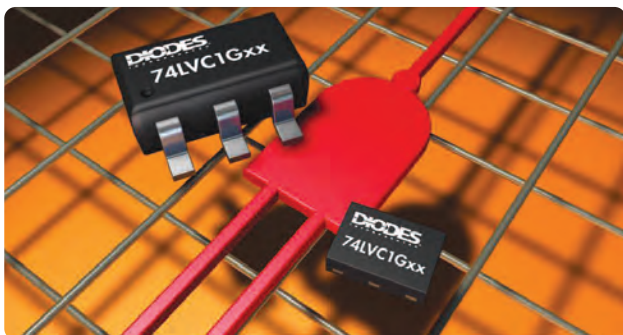
Family	Device Type	Diodes	NXP	Fairchild	TI	Toshiba	ON
AHC1G	Single Gate	74AHC1Gxx	74AHC1Gxx	NC7Sxx	SN74AHC1Gxx	TC7SHxx	MC74VHC1Gxx
AHCT1G	Single Gate	74AHCT1Gxx	74AHCT1Gxx	NC7STxx	SN74AHCT1Gxx	TC7SETxx	MC74VHC1GTxx
AUP1G	Single Gate	74AUP1Gxx	74AUP1Gxx	NC7SPxx	SN74AUP1Gxx	TC7SGxx	–
AUP2G	Dual Gate	74AUP2Gxx	74AUP2Gxx	NC7WPxx	SN74AUP2Gxx	–	–
LVC1G	Single Gate	74LVC1Gxx	74LVC1Gxx	NC7SZxx	SN74LVC1Gxx	TC7SZxx	NL17SZxx
LVC1T	Single Translator	74LVC1Txx	74LVC1Txx	–	SN74LVC1Txx	–	–
LVC2G	Dual Gate	74LVC2Gxx	74LVC2Gxx	NC7WZxx	SN74LVC2Gxx	TC7WZxx	NL17WZxx
AHC	Standard Logic	74AHCxxx	74AHCxxx	VHCxxx	SN74AHCxxx	VHCxxx	VHCxxx
AHCT	Standard Logic	74AHCTxxx	74AHCTxxx	VHCTxxx	SN74AHCTxxx	VHCTxxx	VHCTxxx
HC	Standard Logic	74HCxxx	74HCxxx	HCxxx	SN74HCxxx	HCxxx	HCxxx
HCT	Standard Logic	74HCTxxx	74HCTxxx	HCTxxx	SN74HCTxxx	HCTxxx	HCTxxx
LVC	Standard Logic	74LVCxxx	74LVCxxx	LCxxx	SN74LVCxxx	LCxxx	LCxxx

Package Selection

Package	Pin Count	Package Code	Typical Dimensions				
			Width (mm)	Length (mm)	Height (mm)	Area (mm) ²	Pitch (mm)
SOT25	5	W5	2.8	3	1.1	8.4	0.95
SOT26	6	W6	2.8	3	1.1	8.4	0.95
SOT353	5	SE	2.1	2.15	1	4.515	0.65
SOT363	6	DW	2.1	2.15	1	4.515	0.65
SOT553	5	Z	1.6	1.6	0.6	2.56	0.5
SOT563	6	Z6	1.6	1.6	0.6	2.56	0.5
DFN0808	4+1	FS3	0.8	0.8	0.3	0.64	0.5
DFN0910	6	FW3	0.9	1	0.3	0.9	0.3
DFN1010	6	FW4	1	1	0.4	1	0.35
DFN1210	8	RA3	1.2	1	0.3	1.2	0.3
DFN1409	6	FX4	1.4	0.9	0.37	1.26	0.5
DFN1410	6	FZ4	1.4	1	0.4	1.4	0.5
DFN2030	8	RB5	2	3	0.5	6	0.5
SOIC-14	14	S14	8.6	6	1.6	51.6	1.27
TSSOP-14	14	T14	6.4	5	1	32	0.65
SOIC-16	16	S16	9.9	6	1.6	59.4	1.27
TSSOP-16	16	T16	6.4	5	1	32	0.65
QFN-20	20	Q20	4.5	2.5	0.85	11.25	0.5

The taping orientation is located on our website at www.diodes.com/datasheets/ap02007.pdf

THE DIODES ADVANTAGE



The Diodes Advantage

- **Package Choice**
Options for legacy packages or cutting edge space-saving designs.
- **Lead Free / ROHS Compliant**
No excessive lead or halides.
- **Chip Scale Replacement**
The smallest DFN packages are space equivalent and more mechanically robust.

Package Codes

Family	Pin Count	Diodes	NXP	Fairchild	TI	Toshiba	ON
SOT25	5	W5	GV	M5	DBV	F	DTT
SOT26	6	W6	GV	M6	DBV	F	DTT
SOT353	5	SE	GW	P5	DCK	FU	DFT
SOT363	6	DW	GW	P6	DCK	FU	DFT
SOT553	5	Z			DRL	FE	XV5
SOT563	6	Z6			DRL		XV6
DFN0808	4+1	FS3	GX				
DFN0910	6	FW3	GN				
DFN1010	6	FW4	GF	FH	DSF		
DFN1210	8	RA3	GN				
DFN1409	6	FX4			YZP‡		
DFN1410	6	FZ4	GM	L6	DRY		
DFN2030	8	RB5	GD				
SOIC-14	14	S14	D	M	D	FN	D
TSSOP-14	14	T14	PW	MTC	PW	FT	DT
SOIC-16	16	S16	D	M	D	FN	D
TSSOP-16	16	T16	PW	MTC	PW	FT	DT
QFN-20	20	Q20	ABQ				

‡DFN1409 is a chip scale alternative

Logic Products Cross Reference

Diodes Device	Package	Description	Toshiba	TI	NXP	ON	Fairchild
74AHC1G00SE-7	SOT353	Single 2-Input NAND Gate	TC7SH00FU	SN74AHC1G00DCKR	74AHC1G00GW,125	MC74VHC1G00DF	NC7500P5X
74AHC1G00W5-7	SOT25	Single 2-Input NAND Gate	TC7SH00F	SN74AHC1G00DBVR	74AHC1G00GV,125	MC74VHC1G00DT	NC7500M5X
74AHC1G02SE-7	SOT353	Single 2-Input NOR Gate	TC7SH02FU	SN74AHC1G02DCKR	74AHC1G02GW,125	MC74VHC1G02DF	NC7502P5X
74AHC1G02W5-7	SOT25	Single 2-Input NOR Gate	TC7SH02F	SN74AHC1G02DBVR	74AHC1G02GV,125	MC74VHC1G02DT	NC7502M5X
74AHC1G04SE-7	SOT353	Single Inverter	TC7SH04FU	SN74AHC1G04DCKR	74AHC1G04GW,125	MC74VHC1G04DF	NC7504P5X
74AHC1G04W5-7	SOT25	Single Inverter	TC7SH04F	SN74AHC1G04DBVR	74AHC1G04GV,125	MC74VHC1G04DT	NC7504M5X
74AHC1G08SE-7	SOT353	Single 2-Input AND Gate	TC7SH08FU	SN74AHC1G08DCKR	74AHC1G08GW,125	MC74VHC1G08DF	NC7508P5X
74AHC1G08W5-7	SOT25	Single 2-Input AND Gate	TC7SH08F	SN74AHC1G08DBVR	74AHC1G08GV,125	MC74VHC1G08DT	NC7508M5X
74AHC1G09SE-7	SOT353	Single 2-Input AND Gate with Open-Drain Output	TC7SH09FU	SN74AHC1G09DCKR	74AHC1G09GW,125	MC74VHC1G09DF	
74AHC1G09W5-7	SOT25	Single 2-Input AND Gate with Open-Drain Output		SN74AHC1G09DBVR	74AHC1G09GV,125	MC74VHC1G09DT	
74AHC1G14SE-7	SOT353	Single Schmitt Trigger Inverter	TC7SH14FU	SN74AHC1G14DCKR	74AHC1G14GW,125	MC74VHC1G14DF	NC7514P5X
74AHC1G14W5-7	SOT25	Single Schmitt Trigger Inverter	TC7SH14F	SN74AHC1G14DBVR	74AHC1G14GV,125	MC74VHC1G14DT	NC7514M5X
74AHC1G32SE-7	SOT353	Single 2-Input OR Gate	TC7SH32FU	SN74AHC1G32DCKR	74AHC1G32GW,125	MC74VHC1G32DF	NC7532P5X
74AHC1G32W5-7	SOT25	Single 2-Input OR Gate	TC7SH32F	SN74AHC1G32DBVR	74AHC1G32GV,125	MC74VHC1G32DT	NC7532M5X
74AHC1G86SE-7	SOT353	Single 2-Input EXCLUSIVE OR Gate	TC7SH86FU	SN74AHC1G86DCKR	74AHC1G86GW,125	MC74VHC1G86DF	NC7586P5X
74AHC1G86W5-7	SOT25	Single 2-Input EXCLUSIVE OR Gate	TC7SH86F	SN74AHC1G86DBVR	74AHC1G86GV,125	MC74VHC1G86DT	NC7586M5X
74AHC1G125SE-7	SOT353	Single Buffer with 3-State Output OE LOW	TC7SH125FU	SN74AHC1G125DCKR	74AHC1G125GW,125	MC74VHC1G125DF	
74AHC1G125W5-7	SOT25	Single Buffer with 3-State Output OE LOW	TC7SH125F	SN74AHC1G125DBVR	74AHC1G125GV,125	MC74VHC1G125DT	
74AHC1G126SE-7	SOT353	Single Buffer with 3-State Output OE HIGH	TC7SH126FU	SN74AHC1G126DCKR	74AHC1G126GW,125	MC74VHC1G126DF	
74AHC1G126W5-7	SOT25	Single Buffer with 3-State Output OE HIGH	TC7SH126F	SN74AHC1G126DBVR	74AHC1G126GV,125	MC74VHC1G126DT	
74AHC1GU04SE-7	SOT353	Single Unbuffered Inverter	TC7SHU04FU	SN74AHC1GU04DCKR	74AHC1GU04GW,125	MC74VHC1GU04DF	NC75U04P5X
74AHC1GU04W5-7	SOT25	Single Unbuffered Inverter	TC7SHU04F	SN74AHC1GU04DBVR	74AHC1GU04GV,125	MC74VHC1GU04DT	NC75U04M5X
74AHC00S14-13	SO-14	Quadruple 2-Input NAND Gates		SN74AHC00DR	74AHC00D,118	MC74VHC00DR2	
74AHC00T14-13	TSSOP-14	Quadruple 2-Input NAND Gates		SN74AHC00PWR	74AHC00PW,118	MC74VHC00DTR2	
74AHC04S14-13	SO-14	Hex Inverters		SN74AHC04DR	74AHC04D,118	MC74VHC04DR2	
74AHC04T14-13	TSSOP-14	Hex Inverters		SN74AHC04PWR	74AHC04PW,118	MC74VHC04DTR2	
74AHC05S14-13	SO-14	Hex Inverters with Open-Drain Outputs		SN74AHC05DR	74AHC05D,118	MC74VHC05DR2	
74AHC05T14-13	TSSOP-14	Hex Inverters with Open-Drain Outputs		SN74AHC05PWR	74AHC05PW,118	MC74VHC05DTR2	
74AHC08S14-13	SO-14	Quadruple 2-Input AND Gates		SN74AHC08DR	74AHC08D,118	MC74VHC08DR2	
74AHC08T14-13	TSSOP-14	Quadruple 2-Input AND Gates		SN74AHC08PWR	74AHC08PW,118	MC74VHC08DTR2	
74AHC14S14-13	SO-14	Hex Schmitt Trigger Inverters		SN74AHC14DR	74AHC14D,118	MC74VHC14DR2	
74AHC14T14-13	TSSOP-14	Hex Schmitt Trigger Inverters		SN74AHC14PWR	74AHC14PW,118	MC74VHC14DTR2	
74AHC32S14-13	SO-14	Quadruple 2-Input OR Gates		SN74AHC32DR	74AHC32D,118	MC74VHC32DR2	
74AHC32T14-13	TSSOP-14	Quadruple 2-Input OR Gates		SN74AHC32PWR	74AHC32PW,118	MC74VHC32DTR2	
74AHC86S14-13	SO-14	Quadruple 2-Input EXCLUSIVE OR Gates		SN74AHC86DR	74AHC86D,118	MC74VHC86DR2	
74AHC86T14-13	TSSOP-14	Quadruple 2-Input EXCLUSIVE OR Gates		SN74AHC86PWR	74AHC86PW,118	MC74VHC86DTR2	
74AHC125S14-13	SO-14	Quadruple Buffers with 3 State Outputs OE LOW		SN74AHC125DR	74AHC125D,118	MC74VHC125DR2	
74AHC125T14-13	TSSOP-14	Quadruple Buffers with 3 State Outputs OE LOW		SN74AHC125PWR	74AHC125PW,118	MC74VHC125DTR2	
74AHC126S14-13	SO-14	Quadruple Buffers with 3 State Outputs OE HIGH		SN74AHC126DR	74AHC126D,118	MC74VHC126DR2	
74AHC126T14-13	TSSOP-14	Quadruple Buffers with 3 State Outputs OE HIGH		SN74AHC126PWR	74AHC126PW,118	MC74VHC126DTR2	
74AHC138S16-13	SO-16	3-to-8 Line Decoder Demultiplexer	TC74VHC138FN	SN74AHC138DR	74AHC138D	MC74VHC138DR2G	74VHC138MX
74AHC138T16-13	TSSOP-16	3-to-8 Line Decoder Demultiplexer	TC74VHC138FT	SN74AHC138PWR	74AHC138PW	MC74VHC138DTR2G	74VHC138MTCX
74AHC594S16-13	SO-16	8-Bit Shift Register 8-Bit Output Register		SN74AHC594DR	74AHC594D		
74AHC594T16-13	TSSOP-16	8-Bit Shift Register 8-Bit Output Register		SN74AHC594PWR	74AHC594PW		
74AHC595S16-13	SO-16	8-Bit Shift Register 8-Bit Output Register	TC74VHC595FN	SN74AHC595DR	74AHC595D	MC74VHC595DR2G	74VHC595MX
74AHC595T16-13	TSSOP-16	8-Bit Shift Register 8-Bit Output Register	TC74VHC595FT	SN74AHC595PWR	74AHC595PW	MC74VHC595DTR2G	74VHC595MTCX
74AHCU04S14-13	SO-14	Hex Unbuffered Inverters		SN74AHCU04DR	74AHCU04D,118	MC74VHCU04DR2	
74AHCU04T14-13	TSSOP-14	Hex Unbuffered Inverters		SN74AHCU04PWR	74AHCU04PW,118	MC74VHCU04DTR2	
74AHCT1G00SE-7	SOT353	Single 2-Input NAND Gate, TTL Input	TC7SET00FU	SN74AHCT1G00DCKR	74AHCT1G00GW,12	MC74VHC1GT00DF	NC75T00P5X
74AHCT1G00W5-7	SOT25	Single 2-Input NAND Gate, TTL Input	TC7SET00F	SN74AHCT1G00DBVR	74AHCT1G00GV,125	MC74VHC1GT00DT	NC75T00M5X
74AHCT1G02SE-7	SOT353	Single 2-Input NOR Gate, TTL Input	TC7SET02FU	SN74AHCT1G02DCKR	74AHCT1G02GW,125	MC74VHC1GT02DF	NC75T02P5X
74AHCT1G02W5-7	SOT25	Single 2-Input NOR Gate, TTL Input	TC7SET02F	SN74AHCT1G02DBVR	74AHCT1G02GV,125	MC74VHC1GT02DT	NC75T02M5X
74AHCT1G04SE-7	SOT353	Single Inverter, TTL Input	TC7SET04FU	SN74AHCT1G04DCKR	74AHCT1G04GW,125	MC74VHC1GT04DF	NC75T04P5X
74AHCT1G04W5-7	SOT25	Single Inverter, TTL Input	TC7SET04F	SN74AHCT1G04DBVR	74AHCT1G04GV,125	MC74VHC1GT04DT	NC75T04M5X
74AHCT1G08SE-7	SOT353	Single 2-Input AND Gate, TTL Input	TC7SET08FU	SN74AHCT1G08DCKR	74AHCT1G08GW,125	MC74VHC1GT08DF	NC75T08P5X
74AHCT1G08W5-7	SOT25	Single 2-Input AND Gate, TTL Input	TC7SET08F	SN74AHCT1G08DBVR	74AHCT1G08GV,125	MC74VHC1GT08DT	NC75T08M5X
74AHCT1G14SE-7	SOT353	Single Schmitt Trigger Inverter, TTL Input	TC7SET14FU	SN74AHCT1G14DCKR	74AHCT1G14GW,125	MC74VHC1GT14DF	
74AHCT1G14W5-7	SOT25	Single Schmitt Trigger Inverter, TTL Input	TC7SET14F	SN74AHCT1G14DBVR	74AHCT1G14GV,125	MC74VHC1GT14DT	
74AHCT1G32SE-7	SOT353	Single 2-Input OR Gate, TTL Input	TC7SET32FU	SN74AHCT1G32DCKR	74AHCT1G32GW,125	MC74VHC1GT32DF	NC75T32P5X
74AHCT1G32W5-7	SOT25	Single 2-Input OR Gate, TTL Input	TC7SET32F	SN74AHCT1G32DBVR	74AHCT1G32GV,125	MC74VHC1GT32DT	NC75T32M5X
74AHCT1G86SE-7	SOT353	Single 2-Input EXCLUSIVE OR Gate, TTL Input	TC7SET86FU	SN74AHCT1G86DCKR	74AHCT1G86GW,125	MC74VHC1GT86DF	NC75T86P5X
74AHCT1G86W5-7	SOT25	Single 2-Input EXCLUSIVE OR Gate, TTL Input	TC7SET86F	SN74AHCT1G86DBVR	74AHCT1G86GV,125	MC74VHC1GT86DT	NC75T86M5X

Logic Products Cross Reference (continued)

Diodes Device	Package	Description	Toshiba	TI	NXP	ON	Fairchild
74AHCT1G125SE-7	SOT353	Single Buffer with 3-State Output OE LOW, TTL Input	TC7SET125FU	SN74AHCT1G125DCKR	74AHCT1G125GW,125	MC74VHC1GT125DF	
74AHCT1G125W5-7	SOT25	Single Buffer with 3-State Output OE LOW, TTL Input	TC7SET125F	SN74AHCT1G125DBVR	74AHCT1G125GV,125	MC74VHC1GT125DT	
74AHCT1G126SE-7	SOT353	Single Buffer with 3-State Output OE HIGH, TTL Input	TC7SET126FU	SN74AHCT1G126DCKR	74AHCT1G126GW,125	MC74VHC1GT126DF	
74AHCT1G126W5-7	SOT25	Single Buffer with 3-State Output OE HIGH, TTL Input	TC7SET126F	SN74AHCT1G126DBVR	74AHCT1G126GV,125	MC74VHC1GT126DT	
74AHCT00S14-13	SO-14	Quadruple 2-Input NAND Gates, TTL Inputs		SN74AHCT00DR	74AHCT00D,118	MC74VHC00DR2	
74AHCT00T14-13	TSSOP-14	Quadruple 2-Input NAND Gates, TTL Inputs		SN74AHCT00PWR	74AHCT00PW,118	MC74VHC00DTR2	
74AHCT04S14-13	SO-14	Hex Inverters, TTL Inputs		SN74AHCT04DR	74AHCT04D,118	MC74VHC04DR2	
74AHCT04T14-13	TSSOP-14	Hex Inverters, TTL Inputs		SN74AHCT04PWR	74AHCT04PW,118	MC74VHC04DTR2	
74AHCT08S14-13	SO-14	Quadruple 2-Input AND Gates, TTL Inputs		SN74AHCT08DR	74AHCT08D,118	MC74VHC08DR2	
74AHCT08T14-13	TSSOP-14	Quadruple 2-Input AND Gates, TTL Inputs		SN74AHCT08PWR	74AHCT08PW,118	MC74VHC08DTR2	
74AHCT14S14-13	SO-14	Hex Schmitt Trigger Inverters, TTL Inputs		SN74AHCT14DR	74AHCT14D,118	MC74VHCT14DR2	
74AHCT14T14-13	TSSOP-14	Hex Schmitt Trigger Inverters, TTL Inputs		SN74AHCT14PWR	74AHCT14PW,118	MC74VHCT14DTR2	
74AHCT32S14-13	SO-14	Quadruple 2-Input OR Gate, TTL Inputs		SN74AHCT32DR	74AHCT32D,118	MC74VHC32DR2	
74AHCT32T14-13	TSSOP-14	Quadruple 2-Input OR Gate, TTL Inputs		SN74AHCT32PWR	74AHCT32PW,118	MC74VHC32DTR2	
74AHCT86S14-13	SO-14	Quadruple 2-Input EXCLUSIVE OR Gates, TTL Inputs		SN74AHCT86DR	74AHCT86D,118	MC74VHC86DR2	
74AHCT86T14-13	TSSOP-14	Quadruple 2-Input EXCLUSIVE OR Gates, TTL Inputs		SN74AHCT86PWR	74AHCT86PW,118	MC74VHC86DTR2	
74AHCT125S14-13	SO-14	Quadruple Buffers with 3 State Outputs OE LOW, TTL Inputs		SN74AHCT125DR	74AHCT125D,118	MC74VHCT125DR2	
74AHCT125T14-13	TSSOP-14	Quadruple Buffers with 3 State Outputs OE LOW, TTL Inputs		SN74AHCT125PWR	74AHCT125PW,118	MC74VHCT125DTR2	
74AHCT126S14-13	SO-14	Quadruple Buffers with 3 State Outputs OE HIGH, TTL Inputs		SN74AHCT126DR	74AHCT126D,118	MC74VHCT126DR2	
74AHCT126T14-13	TSSOP-14	Quadruple Buffers with 3 State Outputs OE HIGH, TTL Inputs		SN74AHCT126PWR	74AHCT126PW,118	MC74VHCT126DTR2	
74AHCT138S16-13	SO-16	3-to-8 Line Decoder Demultiplexer, TTL Inputs	TC74VHCT138AFN	SN74AHCT138DR	74AHCT138D	MC74VHCT138ADR2G	
74AHCT138T16-13	TSSOP-16	3-to-8 Line Decoder Demultiplexer, TTL Inputs	TC74VHCT138AFT	SN74AHCT138PWR	74AHCT138PW	MC74VHCT138ADTRG	
74AHCT594S16-13	SO-16	8-Bit Shift Register 8-Bit Output Register, TTL Inputs		SN74AHCT594DR	74AHCT594D		
74AHCT594T16-13	TSSOP-16	8-Bit Shift Register 8-Bit Output Register, TTL Inputs		SN74AHCT594PWR	74AHCT594PW		
74AHCT595S16-13	SO-16	8-Bit Shift Register 8-Bit Output Register, TTL Inputs		SN74AHCT595DR	74AHCT595D		
74AHCT595T16-13	TSSOP-16	8-Bit Shift Register 8-Bit Output Register, TTL Inputs		SN74AHCT595PWR	74AHCT595PW		
74AUP1G00FS3-7	DFN0808	Single 2-Input NAND Gate			74AUP1G00GX,132		
74AUP1G00FW4-7	DFN1010	Single 2-Input NAND Gate		SN74AUP1G00DSFR	74AUP1G00GF,132		
74AUP1G00FZ4-7	DFN1410	Single 2-Input NAND Gate		SN74AUP1G00DRYR	74AUP1G00GM,115		
74AUP1G00SE-7	SOT353	Single 2-Input NAND Gate		SN74AUP1G00DCKR	74AUP1G00GW,125		
74AUP1G02FS3-7	DFN0808	Single 2-Input NOR Gate			74AUP1G02GX,132		
74AUP1G02FW4-7	DFN1010	Single 2-Input NOR Gate		SN74AUP1G02DSFR	74AUP1G02GF,132		
74AUP1G02FZ4-7	DFN1410	Single 2-Input NOR Gate		SN74AUP1G02DRYR	74AUP1G02GM,115		
74AUP1G02SE-7	SOT353	Single 2-Input NOR Gate		SN74AUP1G02DCKR	74AUP1G02GW,125		
74AUP1G04FS3-7	DFN0808	Single Inverter			74AUP1G04GX,132		
74AUP1G04FW4-7	DFN1010	Single Inverter		SN74AUP1G04DSFR	74AUP1G04GF,132		
74AUP1G04FZ4-7	DFN1410	Single Inverter		SN74AUP1G04DRYR	74AUP1G04GM,115		
74AUP1G04SE-7	SOT353	Single Inverter		SN74AUP1G04DCKR	74AUP1G04GW,125		
74AUP1G06FS3-7	DFN0808	Single Inverter with Open-Drain Output			74AUP1G06GX,132		
74AUP1G06FW4-7	DFN1010	Single Inverter with Open-Drain Output		SN74AUP1G06DSFR	74AUP1G06GF,132		
74AUP1G06FZ4-7	DFN1410	Single Inverter with Open-Drain Output		SN74AUP1G06DRYR	74AUP1G06GM,115		
74AUP1G06SE-7	SOT353	Single Inverter with Open-Drain Output		SN74AUP1G06DCKR	74AUP1G06GW,125		
74AUP1G07FS3-7	DFN0808	Single Buffer with Open-Drain Output			74AUP1G07GX,132		
74AUP1G07FW4-7	DFN1010	Single Buffer with Open-Drain Output		SN74AUP1G07DSFR	74AUP1G07GF,132		
74AUP1G07FZ4-7	DFN1410	Single Buffer with Open-Drain Output		SN74AUP1G07DRYR	74AUP1G07GM,115		
74AUP1G07SE-7	SOT353	Single Buffer with Open-Drain Output		SN74AUP1G07DCKR	74AUP1G07GW,125		
74AUP1G08FS3-7	DFN0808	Single 2-Input AND Gate			74AUP1G08GX,132		
74AUP1G08FW4-7	DFN1010	Single 2-Input AND Gate		SN74AUP1G08DSFR	74AUP1G08GF,132		
74AUP1G08FZ4-7	DFN1410	Single 2-Input AND Gate		SN74AUP1G08DRYR	74AUP1G08GM,115		
74AUP1G08SE-7	SOT353	Single 2-Input AND Gate		SN74AUP1G08DCKR	74AUP1G08GW,125		
74AUP1G09FS3-7	DFN0808	Single 2-Input AND Gate with Open-Drain Output			74AUP1G09GX,132		
74AUP1G09FW4-7	DFN1010	Single 2-Input AND Gate with Open-Drain Output		SN74AUP1G09DSFR	74AUP1G09GF,132		
74AUP1G09FZ4-7	DFN1410	Single 2-Input AND Gate with Open-Drain Output		SN74AUP1G09DRYR	74AUP1G09GM,115		
74AUP1G09SE-7	SOT353	Single 2-Input AND Gate with Open-Drain Output		SN74AUP1G09DCKR	74AUP1G09GW,125		
74AUP1G14FS3-7	DFN0808	Single Schmitt Trigger Inverter			74AUP1G14GX,132		
74AUP1G14FW4-7	DFN1010	Single Schmitt Trigger Inverter		SN74AUP1G14DSFR	74AUP1G14GF,132		
74AUP1G14FZ4-7	DFN1410	Single Schmitt Trigger Inverter		SN74AUP1G14DRYR	74AUP1G14GM,115		
74AUP1G14SE-7	SOT353	Single Schmitt Trigger Inverter		SN74AUP1G14DCKR	74AUP1G14GW,125		
74AUP1G17FS3-7	DFN0808	Single Schmitt Trigger Buffer			74AUP1G17GX,132		
74AUP1G17FW4-7	DFN1010	Single Schmitt Trigger Buffer		SN74AUP1G17DSFR	74AUP1G17GF,132		
74AUP1G17FZ4-7	DFN1410	Single Schmitt Trigger Buffer		SN74AUP1G17DRYR	74AUP1G17GM,115		
74AUP1G17SE-7	SOT353	Single Schmitt Trigger Buffer		SN74AUP1G17DCKR	74AUP1G17GW,125		

Diodes Device	Package	Description	Toshiba	TI	NXP	ON	Fairchild
74AUP1G32FS3-7	DFN0808	Single 2-Input OR Gate			74AUP1G32GX,132		
74AUP1G32FW4-7	DFN1010	Single 2-Input OR Gate		SN74AUP1G32DSFR	74AUP1G32GF,132		
74AUP1G32FZ4-7	DFN1410	Single 2-Input OR Gate		SN74AUP1G32DRYR	74AUP1G32GM,115		
74AUP1G32SE-7	SOT353	Single 2-Input OR Gate		SN74AUP1G32DCKR	74AUP1G32GW,125		
74AUP1G34FS3-7	DFN0808	Single Buffer			74AUP1G34GX,132		
74AUP1G34FW4-7	DFN1010	Single Buffer		SN74AUP1G34DSFR	74AUP1G34GF,132		
74AUP1G34FZ4-7	DFN1410	Single Buffer		SN74AUP1G34DRYR	74AUP1G34GM,115		
74AUP1G34SE-7	SOT353	Single Buffer		SN74AUP1G34DCKR	74AUP1G34GW,125		
74AUP1G86FS3-7	DFN0808	Single 2-Input EXCLUSIVE OR Gate			74AUP1G86GX,132		
74AUP1G86FW4-7	DFN1010	Single 2-Input EXCLUSIVE OR Gate		SN74AUP1G86DSFR	74AUP1G86GF,132		
74AUP1G86FZ4-7	DFN1410	Single 2-Input EXCLUSIVE OR Gate		SN74AUP1G86DRYR	74AUP1G86GM,115		
74AUP1G86SE-7	SOT353	Single 2-Input EXCLUSIVE OR Gate		SN74AUP1G86DCKR	74AUP1G86GW,125		
74AUP1G125FS3-7	DFN0808	Single Buffer with 3-State Output OE LOW			74AUP1G125GX,132		
74AUP1G125FW4-7	DFN1010	Single Buffer with 3-State Output OE LOW		SN74AUP1G125DSFR	74AUP1G125GF,132		
74AUP1G125FZ4-7	DFN1410	Single Buffer with 3-State Output OE LOW		SN74AUP1G125DRYR	74AUP1G125GM,115		
74AUP1G125SE-7	SOT353	Single Buffer with 3-State Output OE LOW		SN74AUP1G125DCKR	74AUP1G125GW,125		
74AUP1G126FS3-7	DFN0808	Single Buffer with 3-State Output OE HIGH			74AUP1G126GX,132		
74AUP1G126FW4-7	DFN1010	Single Buffer with 3-State Output OE HIGH		SN74AUP1G126DSFR	74AUP1G126GF,132		
74AUP1G126FZ4-7	DFN1410	Single Buffer with 3-State Output OE HIGH		SN74AUP1G126DRYR	74AUP1G126GM,115		
74AUP1G126SE-7	SOT353	Single Buffer with 3-State Output OE HIGH		SN74AUP1G126DCKR	74AUP1G126GW,125		
74AUP2G00RA3-7	DFN1210	Dual 2-Input NAND Gates			74AUP2G00GN,132		
74AUP2G00RB5-7	DFN2030	Dual 2-Input NAND Gates			74AUP2G00GD,125		
74AUP2G02RA3-7	DFN1210	Dual 2-Input NOR Gates			74AUP2G02GN,132		
74AUP2G02RB5-7	DFN2030	Dual 2-Input NOR Gates			74AUP2G02GD,125		
74AUP2G04DW-7	SOT363	Dual Inverters			74AUP2G04GW,125		
74AUP2G04FW3-7	DFN0910	Dual Inverters			74AUP2G04GN,132		
74AUP2G06DW-7	SOT363	Dual Inverters with Open-Drain Outputs			74AUP2G06GW,125		
74AUP2G06FW3-7	DFN0910	Dual Inverters with Open-Drain Outputs			74AUP2G06GN,132		
74AUP2G07DW-7	SOT363	Dual Buffers with Open-Drain Outputs			74AUP2G07GW,125		
74AUP2G07FW3-7	DFN0910	Dual Buffers with Open-Drain Outputs			74AUP2G07GN,132		
74AUP2G08RA3-7	DFN1210	Dual 2-Input AND Gates			74AUP2G08GN,132		
74AUP2G08RB5-7	DFN2030	Dual 2-Input AND Gates			74AUP2G08GD,125		
74AUP2G14DW-7	SOT363	Dual Schmitt Trigger Inverters			74AUP2G14GW,125		
74AUP2G14FW3-7	DFN0910	Dual Schmitt Trigger Inverters			74AUP2G14GN,132		
74AUP2G17DW-7	SOT363	Dual Schmitt Trigger Buffers			74AUP2G17GW,125		
74AUP2G17FW3-7	DFN0910	Dual Schmitt Trigger Buffers			74AUP2G17GN,132		
74AUP2G32RA3-7	DFN1210	Dual 2-Input OR Gates			74AUP2G32GN,132		
74AUP2G32RB5-7	DFN2030	Dual 2-Input OR Gates			74AUP2G32GD,125		
74AUP2G34DW-7	SOT363	Dual Buffers			74AUP2G34GW,125		
74AUP2G34FW3-7	DFN0910	Dual Buffers			74AUP2G34GN,132		
74AUP2G86RA3-7	DFN1210	Dual 2-Input EXCLUSIVE OR Gates			74AUP2G86GN,132		
74AUP2G125RA3-7	DFN1210	Dual Buffers with 3-State Outputs OE LOW			74AUP2G125GN,132		
74AUP2G125RB5-7	DFN2030	Dual Buffers with 3-State Outputs OE LOW			74AUP2G125GD,125		
74AUP2G126RA3-7	DFN1210	Dual Buffers with 3-State Outputs OE HIGH			74AUP2G126GN,132		
74AUP2G126RA3-7	DFN1210	Dual Buffers with 3-State Outputs OE HIGH			74AUP2G126GN,132		
74AUP2G126RB5-7	DFN2030	Dual Buffers with 3-State Outputs OE HIGH			74AUP2G126GD,125		
74AUP2G3404FW3-7	DFN0910	Dual - One Buffer & One Inverter			74AUP2G3404GN,132		
74HC00S14-13	SO-14	Quadruple 2-Input NAND Gates		SN74HC00DR	74HC00D,118	MC74HC00DR2	MM74HC00M
74HC00T14-13	TSSOP-14	Quadruple 2-Input NAND Gates		SN74HC00PWR	74HC00PW,118	MC74HC00DTR2	MM74HC00MTC
74HC04S14-13	SO-14	Hex Inverters		SN74HC04DR	74HC04D,118	MC74HC04DR2	MM74HC04M
74HC04T14-13	TSSOP-14	Hex Inverters		SN74HC04PWR	74HC04PW,118	MC74HC04DTR2	MM74HC04MTC
74HC05S14-13	SO-14	Hex Inverters with Open-Drain Outputs		SN74HC05DR	74HC05D,118	MC74HC05DR2	MM74HC05M
74HC05T14-13	TSSOP-14	Hex Inverters with Open-Drain Outputs		SN74HC05PWR	74HC05PW,118	MC74HC05DTR2	MM74HC05MTC
74HC08S14-13	SO-14	Quadruple 2-Input AND Gates		SN74HC08DR	74HC08D,118	MC74HC08DR2	MM74HC08M
74HC08T14-13	TSSOP-14	Quadruple 2-Input AND Gates		SN74HC08PWR	74HC08PW,118	MC74HC08DTR2	MM74HC08MTC
74HC14S14-13	SO-14	Hex Schmitt Trigger Inverters		SN74HC14DR	74HC14D,118	MC74HC14DR2	MM74HC14M
74HC14T14-13	TSSOP-14	Hex Schmitt Trigger Inverters		SN74HC14PWR	74HC14PW,118	MC74HC14DTR2	MM74HC14MTC
74HC32S14-13	SO-14	Quadruple 2-Input OR Gates		SN74HC32DR	74HC32D,118	MC74HC32DR2	MM74HC32M
74HC32T14-13	TSSOP-14	Quadruple 2-Input OR Gates		SN74HC32PWR	74HC32PW,118	MC74HC32DTR2	MM74HC32MTC
74HC86S14-13	SO-14	Quadruple 2-Input EXCLUSIVE OR Gates		SN74HC86DR	74HC86D,118	MC74HC86DR2	MM74HC86M
74HC86T14-13	TSSOP-14	Quadruple 2-Input EXCLUSIVE OR Gates		SN74HC86PWR	74HC86PW,118	MC74HC86DTR2	MM74HC86MTC
74HC125S14-13	SO-14	Quadruple Buffers with 3 State Outputs OE LOW		SN74HC125DR	74HC125D,118	MC74HC125DR2	MM74HC125M

Logic Products Cross Reference (continued)

Diodes Device	Package	Description	Toshiba	TI	NXP	ON	Fairchild
74HC125T14-13	TSSOP-14	Quaduple Buffers with 3-State Outputs OE LOW		SN74HC125PWR	74HC125PW,118	MC74HC125DTR2	MM74HC125MTC
74HC126S14-13	SO-14	Quaduple Buffers with 3-State Outputs OE HIGH		SN74HC126DR	74HC126D,118	MC74HC126DR2	MM74HC126M
74HC126T14-13	TSSOP-14	Quaduple Buffers with 3-State Outputs OE HIGH		SN74HC126PWR	74HC126PW,118	MC74HC126DTR2	MM74HC126MTC
74HC138S16-13	SO-16	3-to-8 Line Decoder Demultiplexer	TC74HC138AFN	SN74HC138DR	74HC138D	MC74HC138ADR2G	MM74HC138MX
74HC138T16-13	TSSOP-16	3-to-8 Line Decoder Demultiplexer		SN74HC138PWR	74HC138PW	MC74HC138ADTR2G	MM74HCT38MTCX
74HC594S16-13	SO-16	8-Bit Shift Register 8-Bit Output Register		SN74HC594DR	74HC594D		
74HC594T16-13	TSSOP-16	8-Bit Shift Register 8-Bit Output Register		SN74HC594PWR	74HC594PW		
74HC595S16-13	SO-16	8-Bit Shift Register 8-Bit Output Register	TC74HC595AFN	SN74HC595DR	74HC595D	MC74HC595ADR2G	MM74HC595MX
74HC595T16-13	TSSOP-16	8-Bit Shift Register 8-Bit Output Register		SN74HC595PWR	74HC595PW	MC74HC595ADTR2G	MM74HC595MTCX
74HCU04S14-13	SO-14	Hex Unbuffered Inverters		SN74HCU04DR	74HCU04D,118	MC74HCU04DR2	MM74HCU04M
74HCU04T14-13	TSSOP-14	Hex Unbuffered Inverters		SN74HCU04PWR	74HCU04PW,118	MC74HCU04DTR2	MM74HCU04MTC
74HCT00S14-13	SO-14	Quaduple 2-Input NAND Gates, TTL Inputs		SN74HCT00DR	74HCT00D,118	MC74HCT00DR2	MM74HCT00M
74HCT00T14-13	TSSOP-14	Quaduple 2-Input NAND Gates, TTL Inputs		SN74HCT00PWR	74HCT00PW,118	MC74HCT00DTR2	MM74HCT00MTC
74HCT04S14-13	SO-14	Hex Inverters, TTL Inputs		SN74HCT04DR	74HCT04D,118	MC74HCT04DR2	MM74HCT04M
74HCT04T14-13	TSSOP-14	Hex Inverters, TTL Inputs		SN74HCT04PWR	74HCT04PW,118	MC74HCT04DTR2	MM74HCT04MTC
74HCT08S14-13	SO-14	Quaduple 2-Input AND Gates, TTL Inputs		SN74HCT08DR	74HCT08D,118	MC74HCT08DR2	MM74HCT08M
74HCT08T14-13	TSSOP-14	Quaduple 2-Input AND Gates, TTL Inputs		SN74HCT08PWR	74HCT08PW,118	MC74HCT08DTR2	MM74HCT08MTC
74HCT14S14-13	SO-14	Hex Schmitt Trigger Inverters, TTL Inputs		SN74HCT14DR	74HCT14D,118	MC74HCT14DR2	MM74HCT14M
74HCT14T14-13	TSSOP-14	Hex Schmitt Trigger Inverters, TTL Inputs		SN74HCT14PWR	74HCT14PW,118	MC74HCT14DTR2	MM74HCT14MTC
74HCT32S14-13	SO-14	Quaduple 2-Input OR Gates, TTL Inputs		SN74HCT32DR	74HCT32D,118	MC74HCT32DR2	MM74HCT32M
74HCT32T14-13	TSSOP-14	Quaduple 2-Input OR Gates, TTL Inputs		SN74HCT32PWR	74HCT32PW,118	MC74HCT32DTR2	MM74HCT32MTC
74HCT86S14-13	SO-14	Quaduple 2-Input EXCLUSIVE OR Gates, TTL Inputs		SN74HCT86DR	74HCT86D,118	MC74HCT86DR2	MM74HCT86M
74HCT86T14-13	TSSOP-14	Quaduple 2-Input EXCLUSIVE OR Gates, TTL Inputs		SN74HCT86PWR	74HCT86PW,118	MC74HCT86DTR2	MM74HCT86MTC
74HCT125S14-13	SO-14	Quaduple Buffers with 3-State Outputs OE LOW, TTL Inputs		SN74HCT125DR	74HCT125D,118	MC74HCT125DR2	MM74HCT125M
74HCT125T14-13	TSSOP-14	Quaduple Buffers with 3-State Outputs OE LOW, TTL Inputs		SN74HCT125PWR	74HCT125PW,118	MC74HCT125DTR2	MM74HCT125MTC
74HCT126S14-13	SO-14	Quaduple Buffers with 3-State Outputs OE HIGH, TTL Inputs		SN74HCT126DR	74HCT126D,118	MC74HCT126DR2	MM74HCT126M
74HCT126T14-13	TSSOP-14	Quaduple Buffers with 3-State Outputs OE HIGH, TTL Inputs		SN74HCT126PWR	74HCT126PW,118	MC74HCT126DTR2	MM74HCT126MTC
74HCT138S16-13	SO-16	3-to-8 Line Decoder Demultiplexer, TTL Inputs	TC74HCT138AFN	SN74HCT138DR	74HCT138D	MC74HCT138ADR2G	MM74HCT138MX
74HCT138T16-13	TSSOP-16	3-to-8 Line Decoder Demultiplexer, TTL Inputs		SN74HCT138PWR	74HCT138PW	MC74HCT138ADTR2G	MM74HCT138MTCX
74HCT594S16-13	SO-16	8-Bit Shift Register 8-Bit Output Register, TTL Inputs			74HCT594D		
74HCT594T16-13	TSSOP-16	8-Bit Shift Register 8-Bit Output Register, TTL Inputs			74HCT594PW		
74HCT595S16-13	SO-16	8-Bit Shift Register 8-Bit Output Register, TTL Inputs			74HCT595D	MC74HCT595ADR2G	
74HCT595T16-13	TSSOP-16	8-Bit Shift Register 8-Bit Output Register, TTL Inputs			74HCT595PW	MC74HCT595ADTR2G	
74LV00A14-13	SO-14	Quaduple 2-Input NAND Gate		SN74LV00ADR	74LV00D,118		
74LV00AT14-13	TSSOP-14	Quaduple 2-Input NAND Gate		SN74LV00APWR	74LV00PW,118		
74LV04A14-13	SO-14	Hex Inverters		SN74LV04ADR	74LV04D,118		
74LV04AT14-13	TSSOP-14	Hex Inverters		SN74LV04APWR	74LV04PW,118		
74LV06A14-13	SO-14	Hex Inverters with Open-Drain Outputs		SN74LV06ADR			
74LV06AT14-13	TSSOP-14	Hex Inverters with Open-Drain Outputs		SN74LV06APWR			
74LV07A14-13	SO-14	Hex Buffers with Open-Drain Outputs		SN74LV07ADR			
74LV07AT14-13	TSSOP-14	Hex Buffers with Open-Drain Outputs		SN74LV07APWR			
74LV08A14-13	SO-14	Quaduple 2-Input AND Gate		SN74LV08ADR	74LV08D,118		
74LV08AT14-13	TSSOP-14	Quaduple 2-Input AND Gate		SN74LV08APWR	74LV08PW,118		
74LV14A14-13	SO-14	Hex Schmidt Trigger Inverters		SN74LV14ADR	74LV14D,118		
74LV14AT14-13	TSSOP-14	Hex Schmidt Trigger Inverters		SN74LV14APWR	74LV14PW,118		
74LV32A14-13	SO-14	Quaduple 2-Input OR Gates		SN74LV32ADR	74LV32D,118		
74LV32AT14-13	TSSOP-14	Quaduple 2-Input OR Gates		SN74LV32APWR	74LV32PW,118		
74LV86A14-13	SO-14	Quaduple 2-Input EXCLUSIVE OR Gates		SN74LV86ADR	74LV86D,118		
74LV86AT14-13	TSSOP-14	Quaduple 2-Input EXCLUSIVE OR Gates		SN74LV86APWR	74LV86PW,118		
74LV125A14-13	SO-14	Quaduple Buffers with 3-State Outputs OE LOW		SN74LV125ADR	74LV125D,118		
74LV125AT14-13	TSSOP-14	Quaduple Buffers with 3-State Outputs OE LOW		SN74LV125APWR	74LV125PW,118		
74LV126A14-13	SO-14	Quaduple Buffers with 3-State Outputs OE HIGH		SN74LV126ADR	74LV126D,118		
74LV126AT14-13	TSSOP-14	Quaduple Buffers with 3-State Outputs OE HIGH		SN74LV126APWR	74LV126PW,118		
74LV240AQ20-13	QFN-20	Octal Buffers/Line Drivers with 3-State Outputs					
74LV241AQ20-13	QFN-20	Octal Buffers/Line Drivers with 3-State Outputs					
74LV244AQ20-13	QFN-20	Octal Buffers/Line Drivers with 3-State Outputs					
74LV245AQ20-13	QFN-20	Octal Bus Transceivers with 3-State Outputs					
74LV273AQ20-13	QFN-20	Octal D-Type Flip-Flops with Clear					
74LV373AQ20-13	QFN-20	Octal Transparent D-Type Latches with 3-State Outputs					
74LV374AQ20-13	QFN-20	Octal D-Type Flip-Flops with 3-State Outputs					
74LV540AQ20-13	QFN-20	Octal Buffers/Line Drivers with 3-State Outputs					
74LV541AQ20-13	QFN-20	Octal Buffers/Line Drivers with 3-State Outputs					

Diodes Device	Package	Description	Toshiba	TI	NXP	ON	Fairchild
74LV573AQ20-13	QFN-20	Octal Transparent D-Type Latches with 3-State Outputs					
74LV574AQ20-13	QFN-20	Octal D-Type Flip-Flops with 3-State Outputs					
74LVC1G00F53-7	DFN0808	Single 2-Input NAND Gate			74LVC1G00GX,132		
74LVC1G00F53-7	DFN0808	Single 2-Input NAND Gate			74LVC1G00GX,132		
74LVC1G00FW4-7	DFN1010	Single 2-Input NAND Gate		SN74LVC1G00DSFR	74LVC1G00GF,132		NC7SZ00FHX
74LVC1G00FZ4-7	DFN1410	Single 2-Input NAND Gate		SN74LVC1G00DRYR	74LVC1G00GM,115		NC7SZ00L6X
74LVC1G00SE-7	SOT353	Single 2-Input NAND Gate	TC7SZ00FU	SN74LVC1G00DCKR	74LVC1G00GW,125	NL17SZ00DF	NC7SZ00PSX
74LVC1G00W5-7	SOT25	Single 2-Input NAND Gate	TC7SZ00F	SN74LVC1G00DBVR	74LVC1G00GV,125		NC7SZ00MSX
74LVC1G00Z-7	SOT553	Single 2-Input NAND Gate	TC7SZ00FE	SN74LVC1G00DRLR		NL17SZ00XV5	
74LVC1G02FW4-7	DFN1010	Single 2-Input NOR Gate		SN74LVC1G02DSFR	74LVC1G02GF,132		NC7SZ02FHX
74LVC1G02FZ4-7	DFN1410	Single 2-Input NOR Gate		SN74LVC1G02DRYR	74LVC1G02GM,115		NC7SZ02L6X
74LVC1G02SE-7	SOT353	Single 2-Input NOR Gate	TC7SZ02FU	SN74LVC1G02DCKR	74LVC1G02GW,125	NL17SZ02DF	NC7SZ02PSX
74LVC1G02W5-7	SOT25	Single 2-Input NOR Gate	TC7SZ02F	SN74LVC1G02DBVR	74LVC1G02GV,125		NC7SZ02MSX
74LVC1G02Z-7	SOT553	Single 2-Input NOR Gate		SN74LVC1G02DRLR		NL17SZ02XV5	
74LVC1G04F53-7	DFN0808	Single Inverter			74LVC1G04GX,132		
74LVC1G04FW4-7	DFN1010	Single Inverter		SN74LVC1G04DSFR	74LVC1G04GF,132		NC7SZ04FHX
74LVC1G04FZ4-7	DFN1410	Single Inverter		SN74LVC1G04DRYR	74LVC1G04GM,115		NC7SZ04L6X
74LVC1G04SE-7	SOT353	Single Inverter	TC7SZ04FU	SN74LVC1G04DCKR	74LVC1G04GW,125	NL17SZ04DF	NC7SZ04PSX
74LVC1G04W5-7	SOT25	Single Inverter	TC7SZ04F	SN74LVC1G04DBVR	74LVC1G04GV,125		NC7SZ04MSX
74LVC1G04Z-7	SOT553	Single Inverter		SN74LVC1G04DRLR		NL17SZ04XV5	
74LVC1G06F53-7	DFN0808	Single Inverter with Open-Drain Output			74LVC1G06GX,132		
74LVC1G06FW4-7	DFN1010	Single Inverter with Open-Drain Output		SN74LVC1G06DSFR	74LVC1G06GF,132		NC7SZ05FHX
74LVC1G06FZ4-7	DFN1410	Single Inverter with Open-Drain Output		SN74LVC1G06DRYR	74LVC1G06GM,115		NC7SZ05L6X
74LVC1G06SE-7	SOT353	Single Inverter with Open-Drain Output	TC7SZ05FU	SN74LVC1G06DCKR	74LVC1G06GW,125	NL17SZ06DF	NC7SZ05PSX
74LVC1G06W5-7	SOT25	Single Inverter with Open-Drain Output	TC7SZ05F	SN74LVC1G06DBVR	74LVC1G06GV,125		NC7SZ05MSX
74LVC1G06Z-7	SOT553	Single Inverter with Open-Drain Output	TC7SZ05FE	SN74LVC1G06DRLR		NL17SZ06XV5	
74LVC1G07F53-7	DFN0808	Single Buffer with Open-Drain Output			74LVC1G07GX,132		
74LVC1G07FW4-7	DFN1010	Single Buffer with Open-Drain Output		SN74LVC1G07DSFR	74LVC1G07GF,132		
74LVC1G07FZ4-7	DFN1410	Single Buffer with Open-Drain Output		SN74LVC1G07DRYR	74LVC1G07GM,115		
74LVC1G07SE-7	SOT353	Single Buffer with Open-Drain Output	TC7SZ07FU	SN74LVC1G07DCKR	74LVC1G07GW,125	NL17SZ07DF	
74LVC1G07W5-7	SOT25	Single Buffer with Open-Drain Output	TC7SZ07F	SN74LVC1G07DBVR	74LVC1G07GV,125		
74LVC1G07Z-7	SOT553	Single Buffer with Open-Drain Output		SN74LVC1G07DRLR		NL17SZ07XV5	
74LVC1G08F53-7	DFN0808	Single 2-Input AND Gate			74LVC1G08GX,132		
74LVC1G08FW4-7	DFN1010	Single 2-Input AND Gate		SN74LVC1G08DSFR	74LVC1G08GF,132		NC7SZ08FHX
74LVC1G08FZ4-7	DFN1410	Single 2-Input AND Gate		SN74LVC1G08DRYR	74LVC1G08GM,115		NC7SZ08L6X
74LVC1G08SE-7	SOT353	Single 2-Input AND Gate	TC7SZ08FU	SN74LVC1G08DCKR	74LVC1G08GW,125	NL17SZ08DF	NC7SZ08PSX
74LVC1G08W5-7	SOT25	Single 2-Input AND Gate	TC7SZ08F	SN74LVC1G08DBVR	74LVC1G08GV,125		NC7SZ08MSX
74LVC1G08Z-7	SOT553	Single 2-Input AND Gate	TC7SZ08FE	SN74LVC1G08DRLR		NL17SZ08XV5	
74LVC1G10DW-7	SOT363	Single 3-Input NAND Gate		SN74LVC1G10DCKR	74LVC1G10GW,125		NC7SZ10P6X
74LVC1G10FW4-7	DFN1010	Single 3-Input NAND Gate			74LVC1G10GF,132		
74LVC1G10FZ4-7	DFN1410	Single 3-Input NAND Gate			74LVC1G10GM,115		NC7SZ10L6X
74LVC1G10W6-7	SOT26	Single 3-Input NAND Gate		SN74LVC1G10DBVR	74LVC1G10GV,125		
74LVC1G11DW-7	SOT363	Single 3-Input AND Gate		SN74LVC1G11DCKR	74LVC1G11GW,125		NC7SZ11P6X
74LVC1G11FW4-7	DFN1010	Single 3-Input AND Gate			74LVC1G11GF,132		
74LVC1G11FZ4-7	DFN1410	Single 3-Input AND Gate			74LVC1G11GM,115		NC7SZ11L6
74LVC1G11W6-7	SOT26	Single 3-Input AND Gate		SN74LVC1G11DBVR	74LVC1G11GV,125		
74LVC1G14F53-7	DFN0808	Single Schmitt Trigger Inverter			74LVC1G14GX,132		
74LVC1G14FW4-7	DFN1010	Single Schmitt Trigger Inverter		SN74LVC1G14DSFR	74LVC1G14GF,132		NC7SZ14FHX
74LVC1G14FZ4-7	DFN1410	Single Schmitt Trigger Inverter		SN74LVC1G14DRYR	74LVC1G14GM,115		NC7SZ14L6X
74LVC1G14SE-7	SOT353	Single Schmitt Trigger Inverter	TC7SZ14FU	SN74LVC1G14DCKR	74LVC1G14GW,125	NL17SZ14DF	NC7SZ14PSX
74LVC1G14W5-7	SOT25	Single Schmitt Trigger Inverter	TC7SZ14F	SN74LVC1G14DBVR	74LVC1G14GV,125		NC7SZ14MSX
74LVC1G17F53-7	DFN0808	Single Schmitt Trigger Buffer			74LVC1G17GX,132		
74LVC1G17FW4-7	DFN1010	Single Schmitt Trigger Buffer		SN74LVC1G17DSFR	74LVC1G17GF,132		
74LVC1G17FZ4-7	DFN1410	Single Schmitt Trigger Buffer		SN74LVC1G17DRYR	74LVC1G17GM,115		
74LVC1G17SE-7	SOT353	Single Schmitt Trigger Buffer	TC7SZ17FU	SN74LVC1G17DCKR	74LVC1G17GW,125	NL17SZ17DF	
74LVC1G17W5-7	SOT25	Single Schmitt Trigger Buffer	TC7SZ17F	SN74LVC1G17DBVR	74LVC1G17GV,125		
74LVC1G32F53-7	DFN0808	Single 2-Input OR Gate			74LVC1G32GX,132		
74LVC1G32FW4-7	DFN1010	Single 2-Input OR Gate		SN74LVC1G32DSFR	74LVC1G32GF,132		NC7SZ32FHX
74LVC1G32FZ4-7	DFN1410	Single 2-Input OR Gate		SN74LVC1G32DRYR	74LVC1G32GM,115		NC7SZ32L6X
74LVC1G32SE-7	SOT353	Single 2-Input OR Gate	TC7SZ32FU	SN74LVC1G32DCKR	74LVC1G32GW,125	NL17SZ32DF	NC7SZ32PSX
74LVC1G32W5-7	SOT25	Single 2-Input OR Gate	TC7SZ32F	SN74LVC1G32DBVR	74LVC1G32GV,125		NC7SZ32MSX
74LVC1G32Z-7	SOT553	Single 2-Input OR Gate	TC7SZ32FE	SN74LVC1G32DRLR		NL17SZ32XV5	

Logic Products Cross Reference (continued)

Diodes Device	Package	Description	Toshiba	TI	NXP	ON	Fairchild
74LVC1G34FS3-7	DFN0808	Single Buffer			74LVC1G34GX,132		
74LVC1G57DW-7	SOT363	Single Configurable Multi-Function Gate		SN74LVC1G57DCKR	74LVC1G57GW,125		NC7SZ57P6X
74LVC1G57FW4-7	DFN1010	Single Configurable Multi-Function Gate		SN74LVC1G57DSFR	74LVC1G57GF,132		NC7SZ57FHX
74LVC1G57FZ4-7	DFN1410	Single Configurable Multi-Function Gate		SN74LVC1G57DRYR	74LVC1G57GM,115		NC7SZ57L6X
74LVC1G57W6-7	SOT26	Single Configurable Multi-Function Gate		SN74LVC1G57DBVR	74LVC1G57GV,125		
74LVC1G58DW-7	SOT363	Single Configurable Multi-Function Gate		SN74LVC1G58DCKR	74LVC1G58GW,125		NC7SZ58P6X
74LVC1G58FW4-7	DFN1010	Single Configurable Multi-Function Gate		SN74LVC1G58DSFR	74LVC1G58GF,132		NC7SZ58FHX
74LVC1G58FZ4-7	DFN1410	Single Configurable Multi-Function Gate		SN74LVC158DRYR	74LVC1G58GM,115		NC7SZ57L6X
74LVC1G58W6-7	SOT26	Single Configurable Multi-Function Gate		SN74LVC1G58DBVR	74LVC1G58GV,125		
74LVC1G86FS3-7	DFN0808	Single 2-Input EXCLUSIVE OR Gate			74LVC1G86GX,132		
74LVC1G86FW4-7	DFN1010	Single 2-Input EXCLUSIVE OR Gate			74LVC1G86GF,132		NC7SZ86FHX
74LVC1G86FZ4-7	DFN1410	Single 2-Input EXCLUSIVE OR Gate			74LVC1G86GM,115		NC7SZ86L6X
74LVC1G86SE-7	SOT353	Single 2-Input EXCLUSIVE OR Gate	TC7SZ86FU	SN74LVC1G86DCKR	74LVC1G86GW,125	NL17SZ86DF	NC7SZ86P5X
74LVC1G86W5-7	SOT25	Single 2-Input EXCLUSIVE OR Gate	TC7SZ86F	SN74LVC1G86DBVR	74LVC1G86GV,125		NC7SZ86M5X
74LVC1G86Z-7	SOT553	Single 2-Input EXCLUSIVE OR Gate	TC7SZ86FE	SN74LVC1G86DRLR		NL17SZ86XV5	
74LVC1G97DW-7	SOT363	Single Configurable Multi-Function Gate		SN74LVC1G97DCKR	74LVC1G97GW,125		
74LVC1G97FW4-7	DFN1010	Single Configurable Multi-Function Gate		SN74LVC1G97DSFR	74LVC1G97GF,132		
74LVC1G97FZ4-7	DFN1410	Single Configurable Multi-Function Gate		SN74LVC197DRYR	74LVC1G97GM,115		
74LVC1G97W6-7	SOT26	Single Configurable Multi-Function Gate		SN74LVC1G97DBVR	74LVC1G97GV,125"		
74LVC1G98DW-7	SOT363	Single Configurable Multi-Function Gate		SN74LVC1G98DCKR	74LVC1G98GW,125		
74LVC1G98FW4-7	DFN1010	Single Configurable Multi-Function Gate		SN74LVC1G98DSFR	74LVC1G98GF,132		
74LVC1G98FZ4-7	DFN1410	Single Configurable Multi-Function Gate		SN74LVC1G98DRYR	74LVC1G98GM,115		
74LVC1G98W6-7	SOT26	Single Configurable Multi-Function Gate		SN74LVC1G98DBVR	74LVC1G98GV,125		
74LVC1G125FS3-7	DFN0808	Single Buffer with 3-State Output OE LOW			74LVC1G125GX,132		
74LVC1G125FW4-7	DFN1010	Single Buffer with 3-State Output OE LOW			74LVC1G125GF,132		
74LVC1G125FZ4-7	DFN1410	Single Buffer with 3-State Output OE LOW		SN74LVC1G125DRYR	74LVC1G125GM,115		NC7SZ125L6X
74LVC1G125SE-7	SOT353	Single Buffer with 3-State Output OE LOW	TC7SZ125FU	SN74LVC1G125DCKR	74LVC1G125GW,125	NL17SZ125DF	NC7SZ125P5X
74LVC1G125W5-7	SOT25	Single Buffer with 3-State Output OE LOW	TC7SZ125F	SN74LVC1G125DBVR	74LVC1G125GV,125	NL17SZ125DT	NC7SZ125M5X
74LVC1G125Z-7	SOT553	Single Buffer with 3-State Output OE LOW	TC7SZ125FE	SN74LVC1G125DRLR		NL17SZ125XV5	
74LVC1G126FS3-7	DFN0808	Single Buffer with 3-State Output OE HIGH			74LVC1G126GX,132		
74LVC1G126FW4-7	DFN1010	Single Buffer with 3-State Output OE HIGH			74LVC1G126GF,132		
74LVC1G126FZ4-7	DFN1410	Single Buffer with 3-State Output OE HIGH		SN74LVC1G126DRYR	74LVC1G126GM,115		NC7SZ126L6X
74LVC1G126SE-7	SOT353	Single Buffer with 3-State Output OE HIGH	TC7SZ126FU	SN74LVC1G126DCKR	74LVC1G126GW,125	NL17SZ126DF	NC7SZ126P5X
74LVC1G126W5-7	SOT25	Single Buffer with 3-State Output OE HIGH	TC7SZ126F	SN74LVC1G126DBVR	74LVC1G126GV,125		NC7SZ126M5X
74LVC1G126Z-7	SOT553	Single Buffer with 3-State Output OE HIGH	TC7SZ126FE	SN74LVC1G126DRLR		NL17SZ126XV5	
74LVC1T45FW4-7	DFN1010	Single Translator Dual Voltage			74LVC1T45GF,132		
74LVC1T45FZ4-7	DFN1410	Single Translator Dual Voltage			74LVC1T45GM,115		
74LVC1T45FX4-7	DFN1409	Single Translator Dual Voltage		74LVC1T45VZPR			
74LVC1T45W6-7	SOT26	Single Translator Dual Voltage		SN74LVC1T45DCKR			
74LVC1T45DW-7	SOT363	Single Translator Dual Voltage		SN74LVC1T45DVBR			
74LVC1T45Z6-7	SOT563	Single Translator Dual Voltage		SN74LVC1T45DRLR			
74LVC2G04DW-7	SOT363	Dual Inverters		SN74LVC2G04DCKR	74LVC2G04GW,125	NL27WZ04DF	NC7WZ04P6X
74LVC2G04FW4-7	DFN1010	Dual Inverters			74LVC2G04GF,132		NC7WZ04FHX
74LVC2G04FZ4-7	DFN1410	Dual Inverters			74LVC2G04GM,115		NC7WZ04L6X
74LVC2G04W6-7	SOT26	Dual Inverters		SN74LVC2G04DBVR	74LVC2G04GV,125	NL27WZ04DT	
74LVC2G06DW-7	SOT363	Dual Inverters with Open-Drain Outputs		SN74LVC2G06DCKR	74LVC2G06GW,125	NL27WZ06DF	
74LVC2G06FW4	DFN1010	Dual Inverters with Open-Drain Outputs			74LVC2G06GF,132		
74LVC2G06FZ4-7	DFN1410	Dual Inverters with Open-Drain Outputs			74LVC2G06GM,115		
74LVC2G06W6-7	SOT26	Dual Inverters with Open-Drain Outputs		SN74LVC2G06DBVR	74LVC2G06GV,125	NL27WZ06DT	
74LVC2G07DW-7	SOT363	Dual Buffers with Open-Drain Outputs		SN74LVC2G07DCKR	74LVC2G07GW,125	NL27WZ07DF	NC7W07P6X
74LVC2G07FW4	DFN1010	Dual Buffers with Open-Drain Outputs			74LVC2G07GF,132		NC7W07FHX
74LVC2G07FZ4-7	DFN1410	Dual Buffers with Open-Drain Outputs			74LVC2G07GM,115		NC7W07L6X
74LVC2G07W6-7	SOT26	Dual Buffers with Open-Drain Outputs		SN74LVC2G07DBVR	74LVC2G07GV,125	NL27WZ07DT	
74LVC2G14DW-7	SOT363	Dual Schmitt Trigger Inverters		SN74LVC2G14DCKR	74LVC2G14GW,125	NL27WZ14DF	NC7WZ14P6X
74LVC2G14FW4-7	DFN1010	Dual Schmitt Trigger Inverters			74LVC2G14GF,132		NC7WZ14FHX
74LVC2G14FZ4-7	DFN1410	Dual Schmitt Trigger Inverters			74LVC2G14GM,115		NC7WZ14L6X
74LVC2G14W6-7	SOT26	Dual Schmitt Trigger Inverters		SN74LVC2G14DBVR	74LVC2G14GV,125	NL27WZ14DT	
74LVC2G17DW-7	SOT363	Dual Schmitt Trigger Buffers		SN74LVC2G17DCKR	74LVC2G17GW,125	NL27WZ17DF	
74LVC2G17FW4	DFN1010	Dual Schmitt Trigger Buffers			74LVC2G17GF,132		NC7WZ17FHX
74LVC2G17FZ4-7	DFN1410	Dual Schmitt Trigger Buffers			74LVC2G17GM,115		NC7WZ17L6X
74LVC2G17W6-7	SOT26	Dual Schmitt Trigger Buffers		SN74LVC2G17DBVR	74LVC2G17GV,125"		
74LVC2G34DW-7	SOT363	Dual Buffers		SN74LVC2G34DCKR	74LVC2G34GW,125	NL27WZ16DF	

Diodes Device	Package	Description	Toshiba	TI	NXP	ON	Fairchild
74LVC2G34FW4-7	DFN1010	Dual Buffers			74LVC2G34GF,132		NC7WZ34FHX
74LVC2G34FZ4-7	DFN1410	Dual Buffers			74LVC2G34GM,115		NC7WZ34L6X
74LVC2G34W6-7	SOT26	Dual Buffer		SN74LVC2G34DBVR	74LVC2G34GV,125	NL27WZ16DT	
74LVC00A514-13	SO-14	Quadruple 2-Input NAND Gates	TC74LCX00FN	SN74LVC00ADR	74LVC00AD,118	MC74LCX00DR2	74LCX00M
74LVC00AT14-13	TSSOP-14	Quadruple 2-Input NAND Gates	TC74LCX00FT	SN74LVC00APWR	74LVC00APW,118	MC74LCX00DTR2	74LCX00MTC
74LVC04A514-13	SO-14	Hex Inverters	TC74LCX04FN	SN74LVC04ADR	74LVC04AD,118	MC74LCX04DR2	74LCX04M
74LVC04AT14-13	TSSOP-14	Hex Inverters	TC74LCX04FT	SN74LVC04APWR	74LVC04APW,118	MC74LCX04DTR2	74LCX04MTC
74LVC06A514-13	SO-14	Hex Inverters with Open-Drain Outputs	TC74LCX05FN	SN74LVC06ADR	74LVC06AD,118	MC74LCX06DR2	74LCX06M
74LVC06AT14-13	TSSOP-14	Hex Inverters with Open-Drain Outputs	TC74LCX05FT	SN74LVC06APWR	74LVC06APW,118	MC74LCX06DTR2	74LCX06MTC
74LVC07A514-13	SO-14	Hex Buffers with Open-Drain Outputs	TC74LCX07FN	SN74LVC07ADR	74LVC07AD,118	MC74LCX07DR2	74LCX07M
74LVC07AT14-13	TSSOP-14	Hex Buffers with Open-Drain Outputs	TC74LCX07FT	SN74LVC07APWR	74LVC07APW,118	MC74LCX07DTR2	74LCX07MTC
74LVC08A514-13	SO-14	Quadruple 2-Input AND Gates	TC74LCX08FN	SN74LVC08ADR	74LVC08AD,118	MC74LCX08DR2	74LCX08M
74LVC08AT14-13	TSSOP-14	Quadruple 2-Input AND Gates	TC74LCX08FT	SN74LVC08APWR	74LVC08APW,118	MC74LCX08DTR2	74LCX08MTC
74LVC14A514-13	SO-14	Hex Schmitt Trigger Inverters	TC74LCX14FN	SN74LVC14ADR	74LVC14AD,118	MC74LCX14DR2	74LCX14M
74LVC14AT14-13	TSSOP-14	Hex Schmitt Trigger Inverters	TC74LCX14FT	SN74LVC14APWR	74LVC14APW,118	MC74LCX14DTR2	74LCX14MTC
74LVC32A514-13	SO-14	Quadruple 2-Input OR Gates	TC74LCX32FN	SN74LVC32ADR	74LVC32AD,118	MC74LCX32DR2	74LCX32M
74LVC32AT14-13	TSSOP-14	Quadruple 2-Input OR Gates	TC74LCX32FT	SN74LVC32APWR	74LVC32APW,118	MC74LCX32DTR2	74LCX32MTC
74LVC86A514-13	SO-14	Quadruple 2-Input EXCLUSIVE OR Gates	TC74LCX86FN	SN74LVC86ADR	74LVC86AD,118	MC74LCX86DR2	74LCX86M
74LVC86AT14-13	TSSOP-14	Quadruple 2-Input EXCLUSIVE OR Gates	TC74LCX86FT	SN74LVC86APWR	74LVC86APW,118	MC74LCX086DTR2	74LCX086MTC
74LVC125A514-13	SO-14	Quadruple Buffers with 3-State Outputs OE LOW	TC74LCX125FN	SN74LVC125ADR	74LVC125AD,118	MC74LCX125DR2	74LCX125M
74LVC125AT14-13	TSSOP-14	Quadruple Buffers with 3-State Outputs OE LOW	TC74LCX125FT	SN74LVC125APWR	74LVC125APW,118	MC74LCX125DTR2	74LCX125MTC
74LVC126A514-13	SO-14	Quadruple Buffers with 3-State Outputs OE HIGH	TC74LCX126FN	SN74LVC126ADR	74LVC126AD,118	MC74LCX126DR2	74LCX126M
74LVC126AT14-13	TSSOP-14	Quadruple Buffers with 3-State Outputs OE HIGH	TC74LCX126FT	SN74LVC126APWR	74LVC126APW,118	MC74LCX126DTR2	74LCX126MTC
74LVC240AQ20-13	QFN-20	Octal Buffers/Line Drivers with 3-State Outputs			74LVC240ABQ		
74LVC241AQ20-13	QFN-20	Octal Buffers/Line Drivers with 3-State Outputs					
74LVC244AQ20-13	QFN-20	Octal Buffers/Line Drivers with 3-State Outputs			74LVC244ABQ		
74LVC245AQ20-13	QFN-20	Octal Bus Transceivers with 3-State Outputs			74LVC245ABQ		
74LVC273AQ20-13	QFN-20	Octal D-Type Flip-Flops with Clear			74LVC273BQ		
74LVC373AQ20-13	QFN-20	Octal Transparent D-Type Latches with 3-State Outputs			74LVC373ABQ		
74LVC374AQ20-13	QFN-20	Octal D-Type Flip-Flops with 3-State Outputs			74LVC374ABQ		
74LVC540AQ20-13	QFN-20	Octal Buffers/Line Drivers with 3-State Outputs					
74LVC541AQ20-13	QFN-20	Octal Buffers/Line Drivers with 3-State Outputs			74LVC541ABQ		
74LVC573AQ20-13	QFN-20	Octal Transparent D-Type Latches with 3-State Outputs			74LVC573ABQ		
74LVC574AQ20-13	QFN-20	Octal D-Type Flip-Flops with 3 State Outputs			74LVC574ABQ		
74LVCH244AQ20-13	QFN-20	Octal Buffers/Line Drivers with Bus Hold / 3-State Outputs			74LVCH244ABQ		
74LVCH245AQ20-13	QFN-20	Octal Bus Transceivers with Bus Hold / 3-State Outputs			74LVCH245ABQ		



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