**Vishay Semiconductors** 

High Performance Schottky Rectifier, 1 A



Cathode		Anode
<u> </u>	$\rightarrow$	O

DO-214AC (SMA)

PRODUCT SUMMARY			
Package	DO-214AC (SMA)		
I <sub>F(AV)</sub>	1 A		
V <sub>R</sub>	40 V		
V <sub>F</sub> at I <sub>F</sub>	0.49 V		
I <sub>RM</sub>	26 mA at 125 °C		
T <sub>J</sub> max.	150 °C		
Diode variation	Single die		
E <sub>AS</sub>	3.0 mJ		

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### FEATURES

• Low forward voltage drop



**RoHS** 

COMPLIANT HALOGEN

FREE

- Guard ring for enhanced ruggedness and long term reliability
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### DESCRIPTION

The VS-10MQ040HM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	1	A		
V <sub>RRM</sub>		40	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	120	A		
V <sub>F</sub>	1.5 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.56	V		
TJ	Range	-55 to +150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-10MQ040HM3	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>	40	V		
Maximum working peak reverse voltage	V <sub>RWM</sub>	40	v		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS
Maximum average forward current		50 % duty cycle at $T_L$ = 123 °C, rectangular waveform On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area)		1.5	А
See fig. 4		50 % duty cycle at $T_L$ = 132 °C, rectangular waveform On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area)		1	~
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load	120	А
See fig. 6	IFSM	10 ms sine or 6 ms rect. pulse	$V_{\text{RRM}}$ applied	30	A
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 6 mH		3.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s1.0Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical1.0		А	

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
		1 A	T <sub>.1</sub> = 25 °C	0.54	V
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	1.5 A	1j=23 0	0.62	
See fig. 1	VFM (*)	1 A	T _ 125 °C	0.49	
		1.5 A	T <sub>J</sub> = 125 °C	0.56	
Maximum reverse leakage current	1	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.5	mA
See fig. 2	I <sub>RM</sub>	T <sub>J</sub> = 125 °C		26	
Threshold voltage	V <sub>F(TO)</sub>			0.36	V
Forward slope resistance	r <sub>t</sub>	$T_J = T_J maximum$ 104		mΩ	
Typical junction capacitance	CT	$V_R = 10 V_{DC}$ , $T_J = 25 \text{ °C}$ , test signal = 1 MHz 38		pF	
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body 2.0 r		nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V		V/µs	

#### Note

 $^{(1)}$  Pulse width = 300  $\mu s,$  duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	$T_{J}$ <sup>(1)</sup> , $T_{Stg}$		-55 to +150	°C	
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80	°C/W	
Approximate weight			0.07	g	
Approximate weight			0.002	oz.	
Marking device		Case style SMA (similar D-64)	1	F	

#### Note

(1)  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink



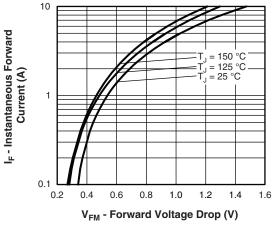
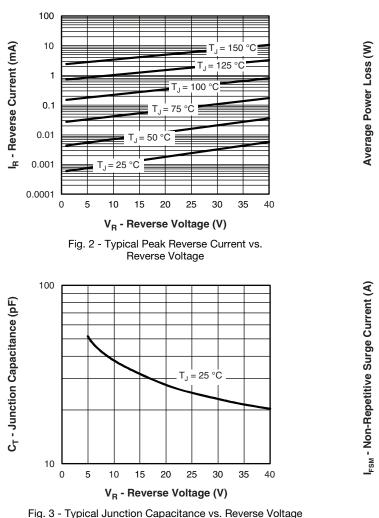


Fig. 1 - Maximum Forward Voltage Drop Characteristics



## VS-10MQ040HM3

## **Vishay Semiconductors**

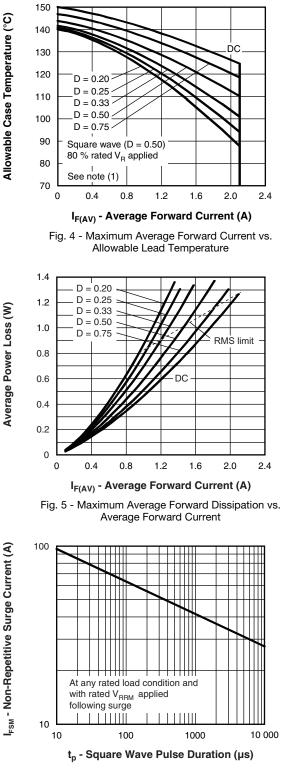


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

Pd = Forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>

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### **ORDERING INFORMATION TABLE**

Device code	VS-	10	М	Q	040	Н	М3
	1	2	3	4	5	6	7
	1 - 2 - 3 -	Cur	nay Serr rent rati		ctors pro	oduct	
	4 -	Q =	Schottk	-			
	5 - 6 -		tage rati AEC-Q	•		)	
	7 -		ironmer	0			nt and t

M3 = Halogen-free, RoHS compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-10MQ040HM3/5AT	5AT	7500	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95400				
Part marking information	www.vishay.com/doc?95403			
Packaging information	www.vishay.com/doc?95404			
SPICE model	www.vishay.com/doc?96007			



# **Outline Dimensions**

## **Vishay Semiconductors**

SMA

### **DIMENSIONS** in inches (millimeters)

DO-214AC (SMA)





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