

SLVS228C - AUGUST 1999-REVISED OCTOBER 2013

3-Pin Supply Voltage Supervisors

Check for Samples: TPS3809J25, TPS3809L30, TPS3809K33, TPS3809I50

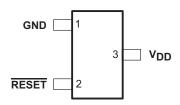
FEATURES

- 3-Pin SOT-23 Package
- Supply Current of 9 µA (Typical)
- **Precision Supply Voltage Monitor** 2.5 V, 3 V, 3.3 V, 5 V
- Pin-For-Pin Compatible With MAX 809
- Temperature Range: -40°C to +85°C

APPLICATIONS

- Applications Using DSPs, Microcontrollers, or Microprocessors
- **Wireless Communication Systems**
- Portable/Battery-Powered Equipment
- **Programmable Controls**
- Intelligent Instruments
- **Industrial Equipment**
- **Notebook/Desktop Computers**
- **Automotive Systems**

DBV PACKAGE TOP VIEW)



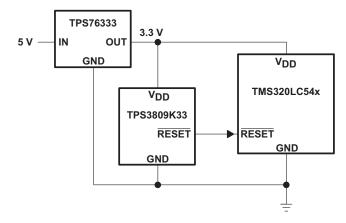
DESCRIPTION

The TPS3809 family of supervisory circuits provides circuit initialization and timing supervision, primarily for DSPs and processor-based systems.

During power-on, RESET is asserted when the supply voltage V_{DD} becomes higher than 1.1 V. Thereafter, the supervisory circuit monitors V_{DD} and keeps RESET active as long as V_{DD} remains below the threshold voltage V_{IT}. An internal timer delays the return of the output to the inactive state (high) to ensure proper system reset. The delay time, $t_{d(typ)}$ = 200 ms, starts after V_{DD} has risen above the threshold voltage V_{IT} . When the supply voltage drops below the threshold voltage V_{IT}, the output becomes active (low) again. No external components are required. All the devices of this family have a fixed sense-threshold voltage V_{IT} set by an internal voltage divider.

The product spectrum is designed for supply voltages of 2.5 V, 3 V, 3.3 V, and 5 V. The circuits are available in a 3-pin SOT-23. The TPS3809 devices are characterized for operation over a temperature range of -40°C to 85°C.

TYPICAL APPLICATION



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These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

AVAILABLE OPTIONS(1)

T _A	DEVICE	NAME	THRESHOLD VOLTAGE	MARKING
	TPS3809J25DBVR	TPS3809J25DBVT	2.25 V	PCZI
4000 to 0500	TPS3809L30DBVR	TPS3809L30DBVT	2.64 V	PDAI
-40°C to 85°C	TPS3809K33DBVR	TPS3809K33DBVT	2.93 V	PDBI
	TPS3809I50DBVR	TPS3809I50DBVT	4.55 V	PDCI

(1) For the most current package and ordering information, see the Package Option Addendum at the end of this data sheet, or visit the device product folder at www.ti.com.

FUNCTION/TRUTH TABLE, TPS3809

V _{DD} >V _{IT}	RESET
0	L
1	н

ORDERING INFORMATION TPS380 9 J 25 DBV R Reel Reel Package Nominal Supply Voltage Functionality Family

ABSOLUTE MAXIMUM RATINGS(1)(2)

Over operating free-air temperature range (unless otherwise noted).

	UNIT
Supply voltage, V _{DD}	7 V
All other pins	–0.3 V to 7 V
Maximum low-output current, I _{OL}	5 mA
Maximum high-output current, I _{OH}	−5 mA
Input-clamp current, I_{IK} ($V_I < 0$ or $V_I > V_{DD}$)	±20 mA
Output-clamp current, I_{OK} ($V_O < 0$ or $V_O > V_{DD}$)	±20 mA
Continuous total power dissipation	See Dissipation Rating Table
Operating free-air temperature range, T _A	-40°C to 85°C
Storage temperature range, T _{stg}	−65°C to 150°C

- (1) Stresses beyond those listed under absolute maximum ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under recommended operating conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) All voltage values are with respect to GND. For reliable operation, the device should not be operated at 7 V for more than t = 1000h continuously.

DISSIPATION RATINGS

PACKAGE	T _A < 25°C	DERATING FACTOR	T _A = 70°C	T _A = 85°C
	POWER RATING	ABOVE T _A < 25°C	POWER RATING	POWER RATING
DBV	437 mW	3.5 mW/°C	280 mW	227 mW

RECOMMENDED OPERATING CONDITIONS

	MIN	MAX	UNIT
Supply voltage, V _{DD}	2	6	V
RESET current sink during startup		50	μΑ
Operating free-air temperature range, T _A	-40	+85	°C

Submit Documentation Feedback



ELECTRICAL CHARACTERISTICS

Over recommended operating free-air temperature range (unless otherwise noted).

PARA	AMETER		TEST CONDITIONS	TPS3800-xx, T	PS3801-xx, TI	PS3802-				
				MIN	MIN TYP MAX					
			$V_{DD} = 2.5 \text{ V to 6 V I}_{OH} = -500 \mu\text{A}$	V _{DD} -0.2						
V_{OH}	High-level output voltage	je	$V_{DD} = 3.3 \text{ V } I_{OH} = -2 \text{ mA}$	V _{DD} -0.4			V			
			$V_{DD} = 6 \text{ V } I_{OH} = -4 \text{ mA}$	V _{DD} -0.4						
			$V_{DD} = 2 \text{ V to 6 V, } I_{OL} = 500 \mu\text{A}$			0.2				
V_{OL}	Low-level output voltage		$V_{DD} = 3.3 \text{ V}, I_{OL} = 2 \text{ mA}$			0.4	V			
			$V_{DD} = 6 \text{ V}, I_{OL} = 4 \text{ mA}$			0.4				
	Power-up reset voltage	(1)	$V_{DD} \ge 1.1 \text{ V}, I_{OL} = 50 \mu\text{A}$			0.2	V			
V _{IT} _	Negative-going input threshold voltage (2)	Negative-going input	TPS3809J25		2.2	2.25	2.3			
			TPS3809L30	T 40%C to 95%C	2.58	2.64	2.7	V		
		reshold voltage (2) TPS3809K33	$T_A = -40$ °C to 85°C	2.87	2.93	2.99	V			
		TPS3809I50		4.45	4.55	0.4 0.2 5 2.3 4 2.7 3 2.99 5 4.65				
		TPS3809J25			30					
.,	Thursday and houstonesis	TPS3809L30			35		\/			
V_{hys}	Threshold hysteresis	TPS3809K33			40		mV			
		TPS3809I50			60					
	Cumply ourrant		V _{DD} = 2 V, output unconnected		9	12				
I _{DD}	Supply current		V _{DD} = 6 V, output unconnected		20	25	μA			
Ci	Input capacitance		$V_I = 0 \text{ V to } V_{DD}$		5		pF			

⁽¹⁾ The lowest supply voltage at which \overline{RESET} becomes active. $t_{r, VDD} \ge 15 \mu s/V$.

TIMING REQUIREMENTS

at R₁ = 1 M Ω , C₁ = 50 pF, T $_{\Delta}$ = 25°C

PAR	AMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
t _w	Pulse width	at V _{DD}	$V_{DD} = V_{IT-} + 0.2 \text{ V}, V_{DD} = V_{IT-} - 0.2 \text{ V}$	3			μs

SWITCHING CHARACTERISTICS

at $R_L = 1$ M Ω , $C_L = 50$ pF, $T_A = 25$ °C

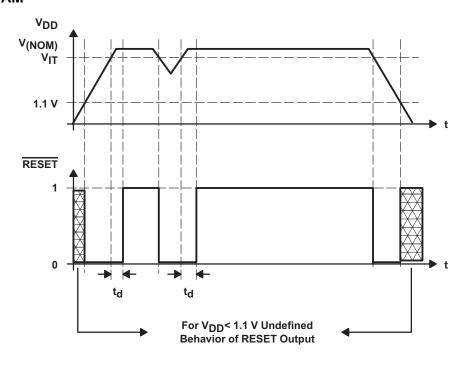
PARAI	METER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
t _d	Delay time		V _{DD} ≥ V _{IT} + 0.2 V, See timing diagram	120	200	280	ms
t _{PHL}	Propagation (delay) time, high-to-low-level output	V _{DD} to RESET delay	$V_{IL} = V_{IT-} - 0.2 \text{ V},$ $V_{IH} = V_{IT-} + 0.2 \text{ V}$		1		μs

Product Folder Links: TPS3809J25 TPS3809L30 TPS3809K33 TPS3809I50

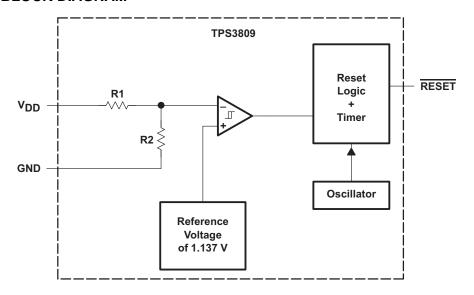
⁽²⁾ To ensure the best stability of the threshold voltage, a bypass capacitor (0.1-µF ceramic) should be placed near the supply terminals.



TIMING DIAGRAM

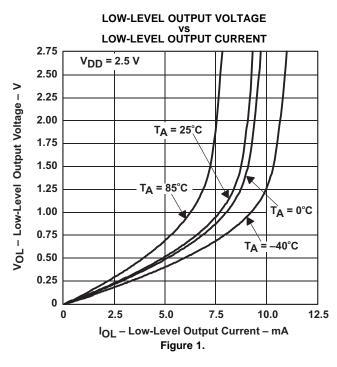


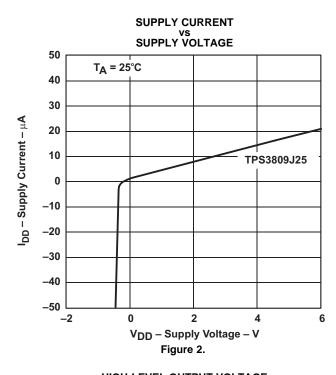
FUNCTIONAL BLOCK DIAGRAM

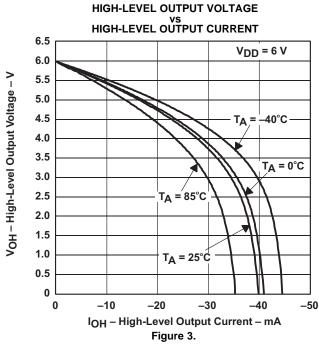


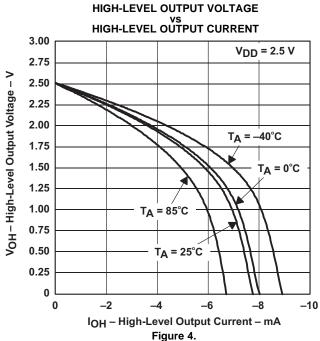


TYPICAL CHARACTERISTICS



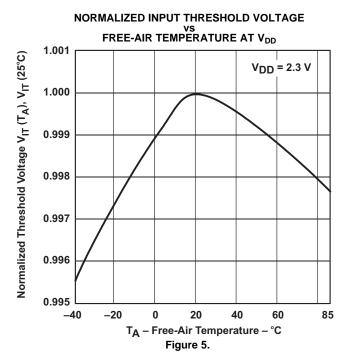


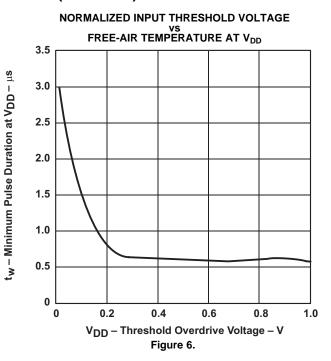






TYPICAL CHARACTERISTICS (continued)





REVISION HISTORY

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision B (July 2012) to Revision C	Page
 Changed front page and page flow to match current standard look and feel Changed "Operating junction temperature range" to "Operating free-air temperature range" in Absolute Ratings (typo) 	e Maximum
Changes from Revision A (October 2010) to Revision B	Page
Changed the Pull-up resistor value, RESET To: RESET current sink during startup in the Recommend Conditions Table	
Changes from Original (August 1999) to Revision A	Page
Added Pull-up resistor value, RESET to the Recommended Operating Conditions Table	2





24-Aug-2018

PACKAGING INFORMATION

Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead/Ball Finish (6)	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
TPS3809I50DBVR	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU Call TI	Level-1-260C-UNLIM	-40 to 85	PDCI	Samples
TPS3809l50DBVRG4	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDCI	Samples
TPS3809l50DBVT	ACTIVE	SOT-23	DBV	3	250	Green (RoHS & no Sb/Br)	CU NIPDAU Call TI	Level-1-260C-UNLIM	-40 to 85	PDCI	Samples
TPS3809J25DBVR	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PCZI	Samples
TPS3809J25DBVT	ACTIVE	SOT-23	DBV	3	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PCZI	Samples
TPS3809J25DBVTG4	ACTIVE	SOT-23	DBV	3	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PCZI	Samples
TPS3809K33DBVR	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDBI	Samples
TPS3809K33DBVRG4	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDBI	Samples
TPS3809K33DBVT	ACTIVE	SOT-23	DBV	3	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDBI	Samples
TPS3809K33DBVTG4	ACTIVE	SOT-23	DBV	3	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDBI	Samples
TPS3809L30DBVR	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDAI	Samples
TPS3809L30DBVRG4	ACTIVE	SOT-23	DBV	3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDAI	Samples
TPS3809L30DBVT	ACTIVE	SOT-23	DBV	3	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDAI	Samples
TPS3809L30DBVTG4	ACTIVE	SOT-23	DBV	3	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	PDAI	Samples

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

PACKAGE OPTION ADDENDUM



24-Aug-2018

(2) RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (CI) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- ⁽⁴⁾ There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead/Ball Finish Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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OTHER QUALIFIED VERSIONS OF TPS3809:

Automotive: TPS3809-Q1

■ Enhanced Product: TPS3809-EP

NOTE: Qualified Version Definitions:

- Automotive Q100 devices qualified for high-reliability automotive applications targeting zero defects
- Enhanced Product Supports Defense, Aerospace and Medical Applications

PACKAGE MATERIALS INFORMATION

www.ti.com 4-Jan-2017

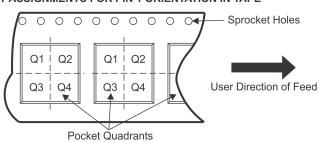
TAPE AND REEL INFORMATION





	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TPS3809I50DBVR	SOT-23	DBV	3	3000	178.0	9.0	3.3	3.2	1.47	4.0	8.0	Q3
TPS3809I50DBVT	SOT-23	DBV	3	250	178.0	9.0	3.3	3.2	1.47	4.0	8.0	Q3
TPS3809J25DBVR	SOT-23	DBV	3	3000	178.0	9.0	3.3	3.2	1.47	4.0	8.0	Q3
TPS3809J25DBVT	SOT-23	DBV	3	250	178.0	9.0	3.3	3.2	1.47	4.0	8.0	Q3
TPS3809K33DBVR	SOT-23	DBV	3	3000	178.0	9.0	3.3	3.2	1.47	4.0	8.0	Q3
TPS3809K33DBVT	SOT-23	DBV	3	250	178.0	9.0	3.3	3.2	1.47	4.0	8.0	Q3
TPS3809L30DBVR	SOT-23	DBV	3	3000	178.0	9.0	3.3	3.2	1.47	4.0	8.0	Q3
TPS3809L30DBVT	SOT-23	DBV	3	250	178.0	8.4	3.3	3.2	1.47	4.0	8.0	Q3

PACKAGE MATERIALS INFORMATION

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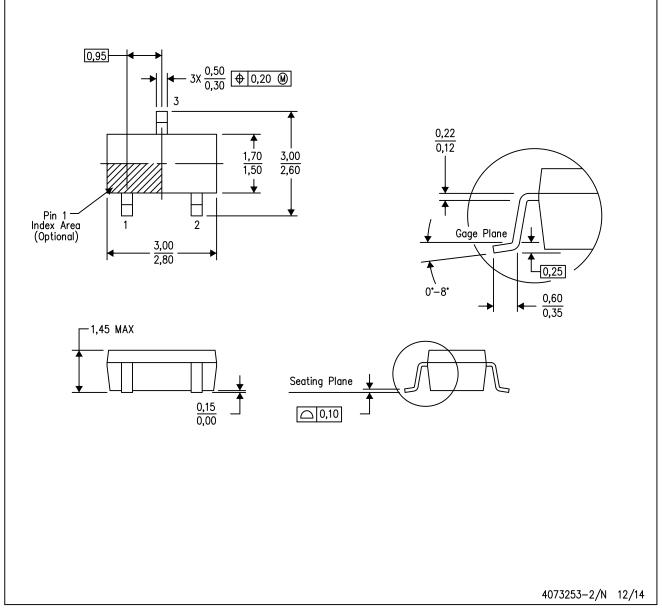


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TPS3809I50DBVR	SOT-23	DBV	3	3000	180.0	180.0	18.0
TPS3809I50DBVT	SOT-23	DBV	3	250	180.0	180.0	18.0
TPS3809J25DBVR	SOT-23	DBV	3	3000	180.0	180.0	18.0
TPS3809J25DBVT	SOT-23	DBV	3	250	180.0	180.0	18.0
TPS3809K33DBVR	SOT-23	DBV	3	3000	180.0	180.0	18.0
TPS3809K33DBVT	SOT-23	DBV	3	250	180.0	180.0	18.0
TPS3809L30DBVR	SOT-23	DBV	3	3000	180.0	180.0	18.0
TPS3809L30DBVT	SOT-23	DBV	3	250	180.0	180.0	18.0

DBV (R-PDSO-G3)

PLASTIC SMALL-OUTLINE PACKAGE



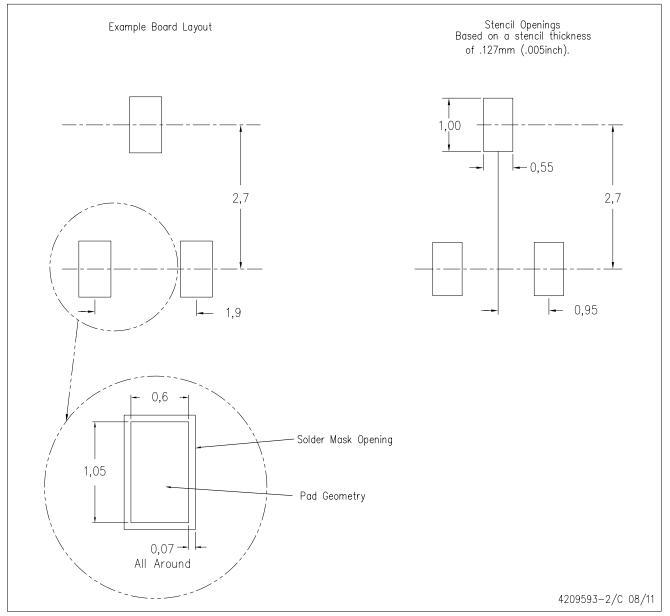
NOTES:

- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.



DBV (R-PDSO-G3)

PLASTIC SMALL OUTLINE



NOTES:

- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Customers should place a note on the circuit board fabrication drawing not to alter the center solder mask defined pad.
- D. Publication IPC-7351 is recommended for alternate designs.
- E. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC-7525 for other stencil recommendations.



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