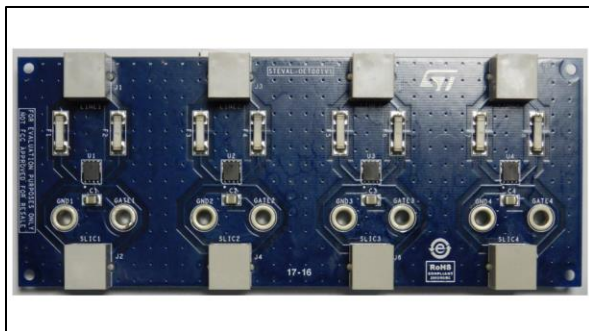


LCP154DJF board to validate lightning protection for SLIC transceivers

Data brief



Features

- 4 line protection for SLIC transceivers
- Compliant with ITU-T K20/21/45 and GR1089-Core
- Programmable transient suppressor
- RoHS compliant

Description

The STEVAL-OET001V1 board is designed to validate lightning protection for SLIC transceivers. The maximum peak pulse current for different waveform is given in the LCP154DJF datasheet.

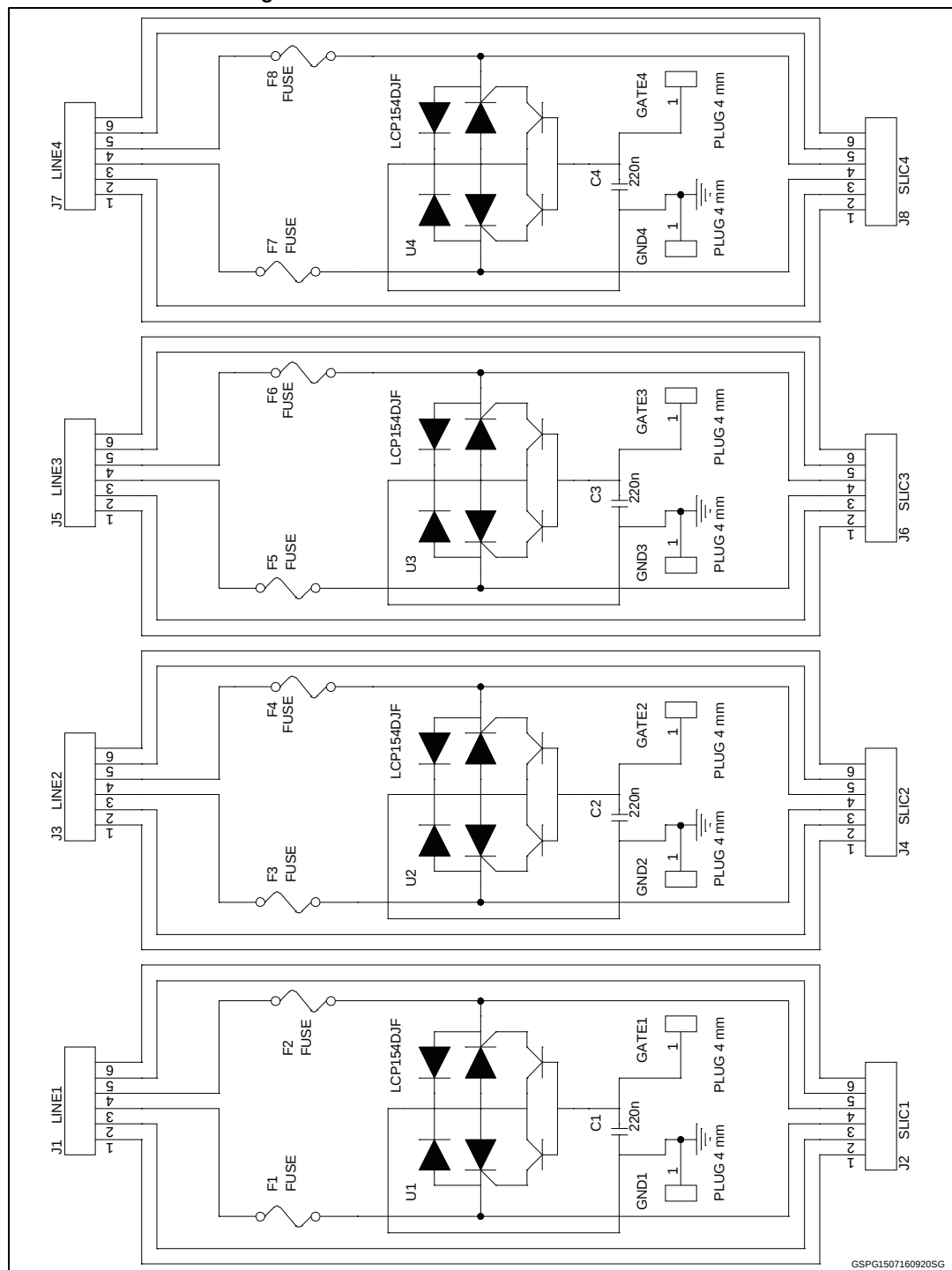
The STEVAL-OET001V1 is able to protect 4 lines; several boards can be used if the number of lines is higher.

The STEVAL-OET001V1 board specifications comply with ITU-T K20/21/45 and GR1089-Core associated with Cooper Bussmann Telecom Circuit Protector fuse TCP 1.25 A.

1 Schematic diagrams

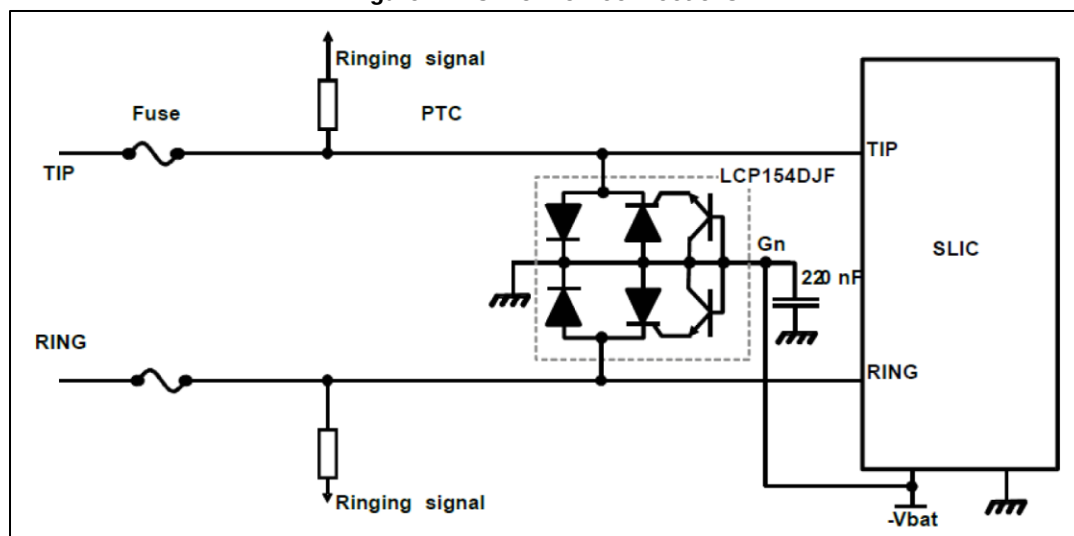
The following figure shows the board schematic. Each line has 1 x LCP154DJF, 1 x 220 nF capacitor connected between GND and GATE, 1 female RJ11 to be connected to the line (written on demo board: LINE1, LINE2, LINE3 and LINE4) and 1 female RJ11 to be connected to the SLIC (written on board SLIC1, SLIC 2, SLIC3 and SLIC4).

Figure 1: STEVAL-OET001V1 circuit schematic



The figure below shows the connection of LCP154DJF associated with a SLIC.

Figure 2: LCP154DJF connections



Application note AN4876 available on www.st.com provides further LCP154DJF implementation and performance details.

2 Revision history

Table 1: Document revision history

Date	Version	Changes
05-Aug-2016	1	Initial release.

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