

## PE77D Series 3.3 V PECL Clock Oscillators

Jan 2015

**Lead Free**



- Pletronics' PE77D Series is a quartz crystal controlled precision square wave generator with a PECL output.
- The package is designed for high density surface mount designs.
- Low cost mass produced oscillator.
- Tape and Reel or cut tape packaging is available.
- 5 x 7 mm LCC Ceramic Package
- Enable/Disable Function on pad 1
- Disable function includes low standby power mode
- 3<sup>rd</sup> Overtone Crystals used
- Improved circuit to minimize oscillator issues such as multi-mode output signal.
- Low Jitter

**Pletronics Inc. certifies this device is in accordance with the  
RoHS 6/6 (2002/95/EC) and WEEE (2002/96/EC) directives.**

Pletronics Inc. guarantees the device does not contain the following:

Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's

Weight of the Device: 0.16 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020D.1

Second Level Interconnect code: e4

### Absolute Maximum Ratings:

Parameter	Unit
V <sub>CC</sub> Supply Voltage	-0.5V to +7.0V
V <sub>i</sub> Input Voltage	-0.5V to V <sub>CC</sub> + 0.5V
V <sub>o</sub> Output Voltage	-0.5V to V <sub>CC</sub> + 0.5V

### Thermal Characteristics

The maximum die or junction temperature is 155°C

The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.

## Part Number:

PE77	45	D	E	V	-125.0M	-XX
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## Part Marking:

							<b>Packaging code or blank</b> <b>T250</b> = 250 per Tape and Reel <b>T500</b> = 500 per Tape and Reel <b>T1K</b> = 1000 per Tape and Reel	<b>PLE PE77</b> <b>FF.FFF M</b> • <b>YMDXX</b>
							<b>Frequency in MHz</b>	or
							<b>Supply Voltage V<sub>cc</sub></b> <b>V</b> = 3.3V $\pm$ 10%	<b>PE7XYWWXX</b> <b>FF.FFF M</b> • <b>PLE XXX</b>
							<b>Optional Enhanced OTR</b> <b>Blank</b> = Temp. range -10 to +70°C <b>C</b> = Temp. range -20 to +70°C <b>E</b> = Temp. range -40 to +85°C	
							<b>Series Model</b>	
							<b>Frequency Stability</b> <b>45</b> = $\pm$ 50 ppm <b>44</b> = $\pm$ 25 ppm <b>20</b> = $\pm$ 20 ppm	
							<b>Series Model</b>	

## Marking Legend:

PLE = Pletronics

FF.FFF M = Frequency in MHz

YYWW or YWW or YMD = Date of Manufacture (year and week, or year-month-day)

All other marking is internal factory codes

Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

## Codes for Date Code YMD

Code	0	1	2	3	4	Code	A	B	C	D	E	F	G	H	J	K	L	M
Year	2010	2011	2012	2013	2014	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	G
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Code	H	J	K	L	M	N	P	R	T	U	V	W	X	Y	Z	
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	

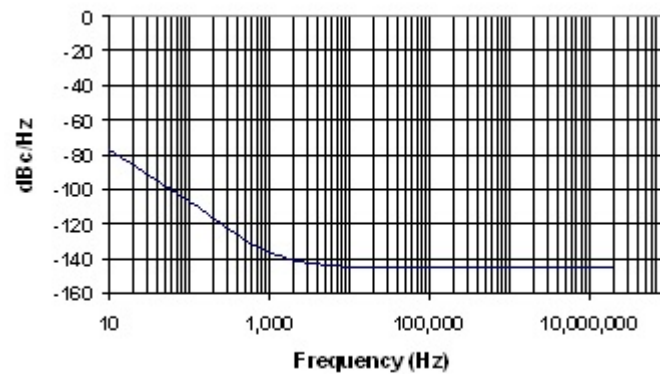
## Electrical Specification for 3.30V $\pm 10\%$ over the specified temperature range and the frequency range of 40 to 325 MHz

Item	Min	Max	Unit	Condition
Frequency Accuracy “45”	-50	+50	ppm	For all supply voltages, load changes, aging for 1 year, shock, vibration and temperatures
“44”	-25	+25		
“20”	-20	+20		
Output Waveform	PECL /ECL			
Output High Level (0°C to 85°C)	2.275	2.420	volts	Referenced to Ground, V <sub>cc</sub> = 3.3 V
	0.975	1.120	volts	Referenced to termination voltage, V <sub>cc</sub> = 3.3 V
	-1.025	-0.880	volts	Referenced to Vcc, V <sub>cc</sub> = 3.3 V
Output High Level (-40°C)	2.216	2.420	volts	Referenced to Ground, V <sub>cc</sub> = 3.3 V
	0.916	1.120	volts	Referenced to termination voltage, V <sub>cc</sub> = 3.3 V
	-1.084	-0.88	volts	Referenced to Vcc, V <sub>cc</sub> = 3.3 V
Output Low Level (0°C to 85°C)	1.490	1.680	volts	Referenced to Ground, V <sub>cc</sub> = 3.3 V
	0.190	0.380	volts	Referenced to termination voltage, V <sub>cc</sub> = 3.3 V
	-1.810	-1.620	volts	Referenced to Vcc, V <sub>cc</sub> = 3.3 V
Output Low Level (-40°C)	1.470	1.745	volts	Referenced to Ground, V <sub>cc</sub> = 3.3 V
	0.170	0.445	volts	Referenced to termination voltage, V <sub>cc</sub> = 3.3 V
	-1.830	-1.555	volts	Referenced to Vcc, V <sub>cc</sub> = 3.3 V
Output Symmetry	45	55	%	at 50% point of V <sub>cc</sub> (See load circuit)
Jitter <sup>1</sup>	-	0.6	pS RMS	12 KHz to 20 MHz from the output frequency
	-	2.8	pS RMS	10 Hz to 1 MHz from the output frequency
Output T <sub>RISE</sub> and T <sub>FALL</sub>	-	0.7	nS	V <sub>th</sub> is 20% and 80% of waveform
V <sub>cc</sub> Supply Current (I <sub>cc</sub> )	-	90	mA	
Enable/Disable Internal Pull-up	50	-	Kohm	to V <sub>cc</sub> , measured with Pad 1 = 0.0 volts
V disable	-	0.6	volts	Referenced to pad 3
V enable	2.40	-	volts	Referenced to pad 3
Output leakage V <sub>OUT</sub> = V <sub>cc</sub>	-10	+10	uA	Pad 1 low, device disabled
V <sub>OUT</sub> = 0V	-10	+10	uA	
Enable time	100	500	nS	Time for output to reach a logic state, the output frequency is correct at the specified Start Time.
Disable time	-	200	nS	Time for output to reach a high Z state
Start up time	-	10	mS	Time for output to reach specified frequency
Operating Temperature Range	-10	+70	°C	Standard Temperature Range
	- 20	+70	°C	Extended Temperature Range “C” Option
	- 40	+85	°C	Extended Temperature Range “E” Option
Storage Temperature Range	-55	+125	°C	
Standby Current I <sub>cc</sub>	-	30	uA	Pad 1 low, device disabled

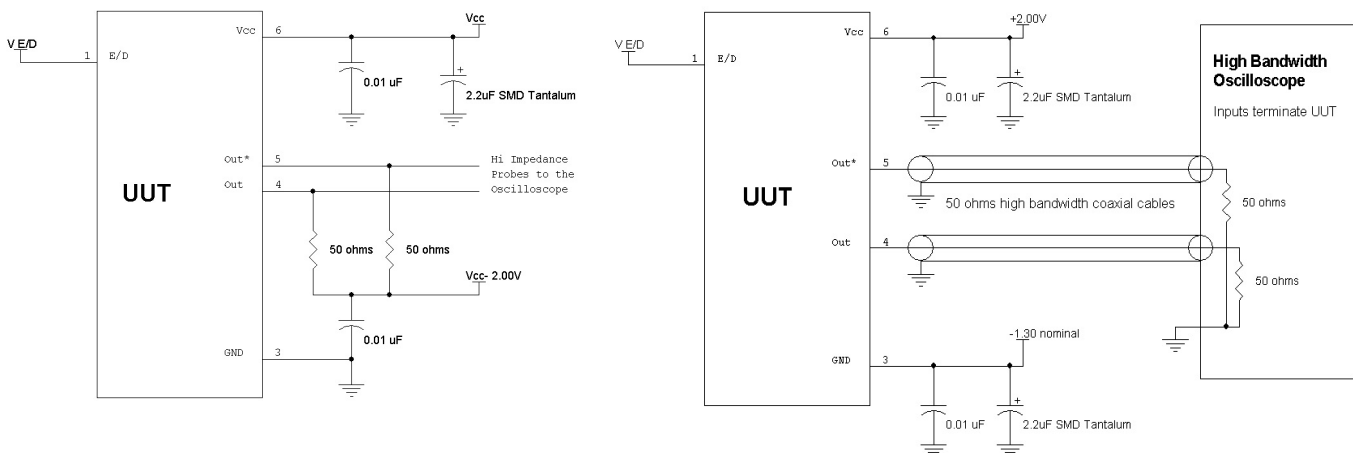
<sup>1</sup> Jitter computed from phase noise data at 125MHz

Specifications with Pad 1 E/D open circuit unless stated otherwise

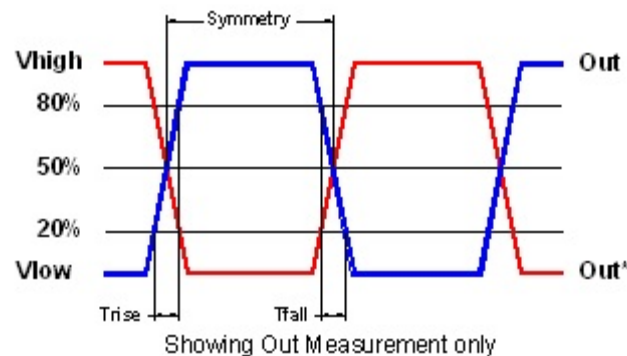
### Typical Phase-Noise Response



### Load Circuit



### Test Waveform



## Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A






## ESD Rating

Model	Minimum Voltage	Conditions
Human Body Model	1500	MIL-STD-883 Method 3115
Charged Device Model	1000	JESD 22-C101

## Package Labeling

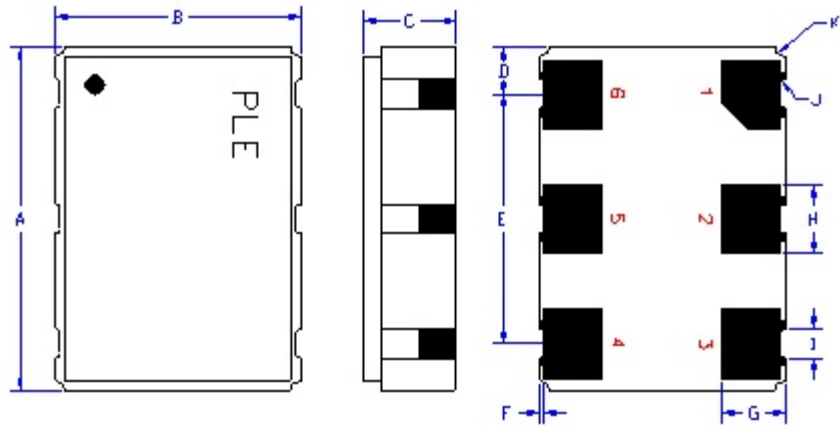
Label is 1" x 2.6" (25.4mm x 66.7mm)  
Font is Courier New  
Bar code is 39-Full ASCII

Label is 1" x 2.6" (25.4mm x 66.7mm)  
Font is Arial

P/N:			
	PE7745DV-100.0M		
Customer P/N:			
	12345678		
Qty:		D/C	
	1000		75409

RoHS Compliant
2nd LvL Interconnect
Category=e4
Max Safe Temp=260C for 10s 2X Max

**Mechanical:**



	Inches	mm
A	0.276 $\pm$ 0.006	7.00 $\pm$ 0.15
B	0.197 $\pm$ 0.006	5.00 $\pm$ 0.15
C	0.067 max	1.70 max
D <sup>1</sup>	0.038	0.96
E <sup>1</sup>	0.200	5.08
F <sup>1</sup>	0.004	0.10
G <sup>1</sup>	0.050	1.27
H <sup>1</sup>	0.055	1.40
I <sup>1</sup>	0.024	0.60
J <sup>1</sup>	0.004R	0.10R
K <sup>1</sup>	0.008R	0.20R

<sup>1</sup> Typical dimensions

Not to Scale

**Note:** C dimension is 1.5 mm typical

**Contacts (pads) :**

Gold 11.8 to 32.7  $\mu$ m (0.3 to 0.83  $\mu$ m) over Nickel 50 to 350  $\mu$ m (1.27 to 8.89  $\mu$ m)

Pad	Function	Note
1	Output Enable/Disable	When this pad is not connected the oscillator shall operate. When this pad is <0.30 volts, the output will be inhibited (high impedance state.) Recommend connecting this pad to V <sub>CC</sub> if the oscillator is to be always on.
2	No connect	There is no internal connection to this pad
3	Ground (GND)	
4	Output	Both outputs must be terminated and biased for proper operation. The ideal termination is 50 ohms connected to 2.0V below the Supply Voltage.
5	Output*	
6	Supply Voltage (V <sub>CC</sub> )	Recommend connecting appropriate power supply bypass capacitors as close as possible.



**Layout and application information**

Recommend connecting Pad 1 and Pad 2 together to permit the design to accept Enable/Disable input on either pad

For Optimum Jitter Performance, Pletronics recommends:

- a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.



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