# **MOSFET** - Power, Single, P-Channel, TO-220

# -60 V, -12 A

## Features

- Low R<sub>DS(on)</sub>
- Rugged Performance
- Fast Switching
- These are Pb-Free Devices\*

## Applications

- Industrial
- Automotive
- Power Supplies

## MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted)

	()				
Parame	Parameter		Symbol	Value	Unit
Drain-to-Source Voltage			V <sub>DSS</sub>	-60	V
Gate-to-Source Voltage			V <sub>GS</sub>	±20	V
Continuous Drain	Steady	$T_C = 25^{\circ}C$	۱ <sub>D</sub>	-12	А
Current (Note 1)	State	$T_{\rm C} = 85^{\circ}{\rm C}$		-9.0	
Power Dissipation (Note 1)		$T_{C} = 25^{\circ}C$	P <sub>D</sub>	62.5	W
Continuous Drain	Steady	$T_A = 25^{\circ}C$	۱ <sub>D</sub>	-2.4	А
Current (Note 1)	State	$T_A = 85^{\circ}C$		-1.8	
Power Dissipation (Note 1)		$T_A = 25^{\circ}C$	PD	2.4	W
Pulsed Drain Current	t <sub>p</sub> =	10 μs	I <sub>DM</sub>	-42	А
Operating Junction and S	torage Ter	nperature	T <sub>J</sub> , T <sub>STG</sub>	–55 to 175	°C
Source Current (Body Dic	ode)		I <sub>S</sub>	-12	А
Single Pulse Drain-to-So Energy (V <sub>DD</sub> = -30 V, I <sub>PK</sub> = -12 A, L = 3.0 m	V <sub>G</sub> = -10	V,	EAS	216	mJ
	ead Temperature for Soldering Purposes (1/8" from case for 10 s)		ΤL	260	°C

# THERMAL RESISTANCE RATINGS

Parameter	Symbol	Мах	Unit
Junction-to-Case	$R_{\theta JC}$	2.4	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$	62.5	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

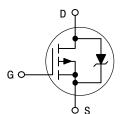


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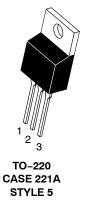
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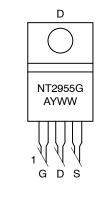
V <sub>(BR)DSS</sub>	V <sub>(BR)DSS</sub> R <sub>DS(on)</sub> Typ	
-60 V	156 mΩ @ −10 V	-12 A

P-Channel



MARKING DIAGRAM & PIN ASSIGNMENT





А	= Assembly Location
Y	= Year
WW	= Work Week
G	= Pb-Free Package

# **ORDERING INFORMATION**

Device Package Shi		Shipping
NTP2955G	TO–220 (Pb–Free)	50 Units / Rail

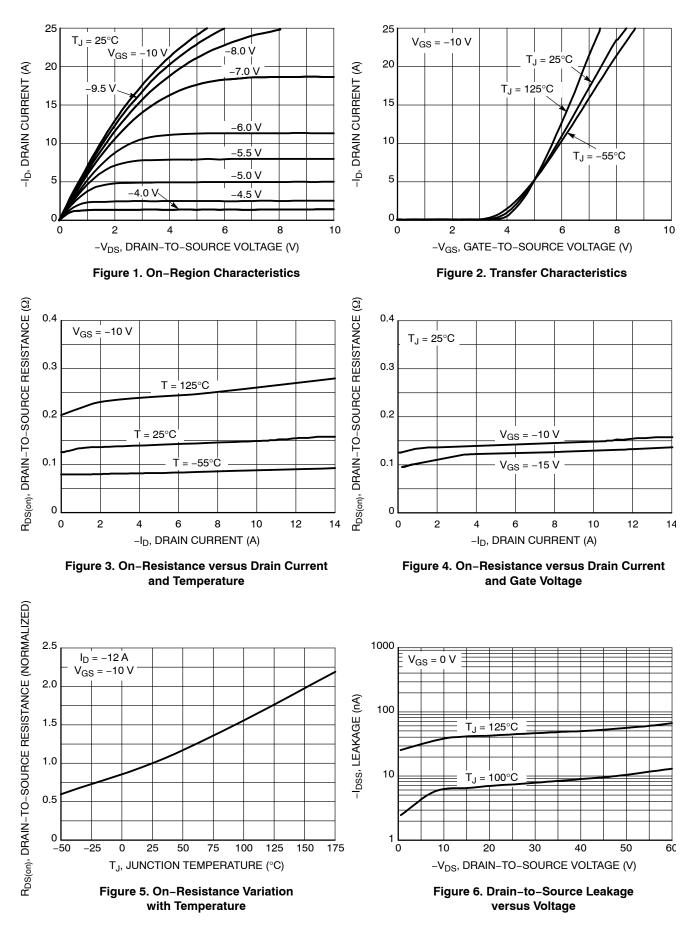
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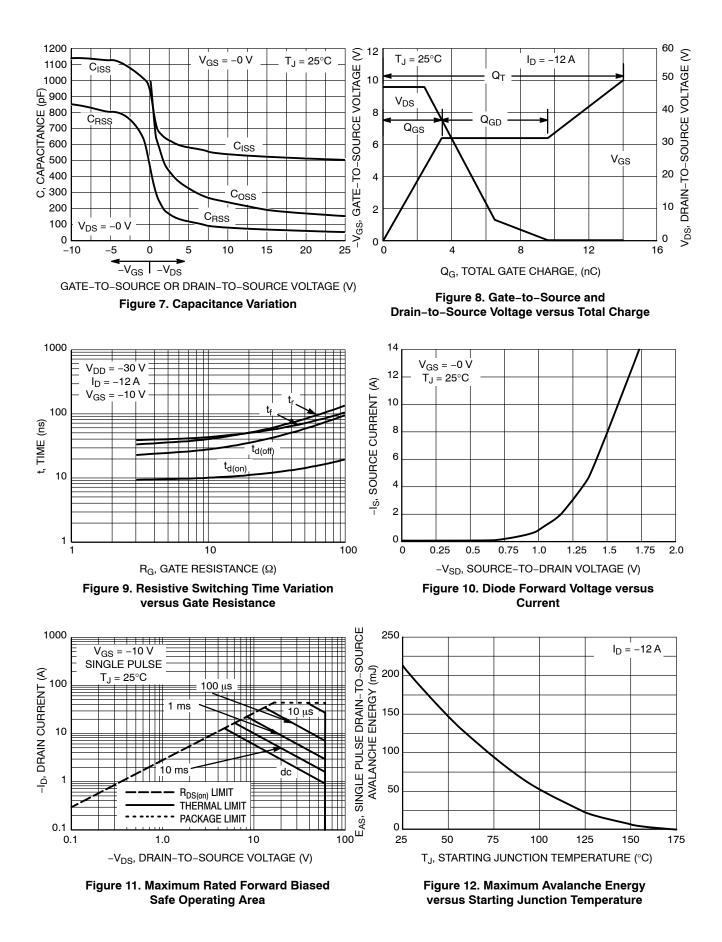
- 1. When surface mounted to an FR4 board using 1 in pad size (Cu. area = 1.127 in sq [1 oz] including traces).
- \*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# ELECTRICAL CHARACTERISTICS (TJ=25°C unless otherwise stated)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS			·				
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = -250 \mu\text{A}$		-60			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /T <sub>J</sub>				67		mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V,	$T_J = 25^{\circ}C$			-1.0	μA
		$V_{DS} = -48 V$	T <sub>J</sub> = 125°C			-10	
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>G</sub>	<sub>iS</sub> = ±20 V			±100	nA
ON CHARACTERISTICS (Note 2)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{GS} = V_{DS}, I_D$	= -250 μA	-2.0		-4.0	V
Negative Threshold Temperature Coefficient	V <sub>GS(TH)</sub> /T <sub>J</sub>				56		mV/°C
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = -10 V,	I <sub>D</sub> = -12 A		156	196	mΩ
Forward Transconductance	<b>9</b> FS	V <sub>DS</sub> = -60 V, I <sub>D</sub> = -12 A			6.0		S
CHARGES AND CAPACITANCES					1		
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> = 0 V, f = 1.0 MHz, V <sub>DS</sub> = -25 V			507	700	pF
Output Capacitance	C <sub>OSS</sub>				150	250	
Reverse Transfer Capacitance	C <sub>RSS</sub>	• 05			48	98	1
Total Gate Charge	Q <sub>G(TOT)</sub>				14		nC
Threshold Gate Charge	Q <sub>G(TH)</sub>	V <sub>GS</sub> = –10 V, V	<sub>DS</sub> = -48 V,		1.6	2.5	
Gate-to-Source Charge	Q <sub>GS</sub>	$I_{\rm D} = -12 {\rm A}$			3.4		
Gate-to-Drain Charge	Q <sub>GD</sub>				6.2		1
SWITCHING CHARACTERISTICS (No	ote 3)		·				
Turn-On Delay Time	t <sub>d(on)</sub>				10	20	ns
Rise Time	t <sub>r</sub>	$V_{GS}$ = -10 V, $V_{DD}$ = -30 V, I <sub>D</sub> = -12 A, R <sub>G</sub> = 9.1 $\Omega$			41	80	
Turn-Off Delay Time	t <sub>d(off)</sub>				27	47	
Fall Time	t <sub>f</sub>				45	85	
DRAIN-SOURCE DIODE CHARACTE	RISTICS						
Forward Diode Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0 V,	$T_J = 25^{\circ}C$		-1.6	-2.0	V
		$I_{\rm S} = -12 \rm A$	T <sub>J</sub> = 125°C		-1.36		
Reverse Recovery Time	t <sub>RR</sub>	V <sub>GS</sub> = 0 V, dl <sub>S</sub> /dt = 100 A/µs,			53		
Charge Time	ta				42		ns
Discharge Time	t <sub>b</sub>	$I_{\rm S} = -1$			12		
Reverse Recovery Charge	Q <sub>RR</sub>				126		nC

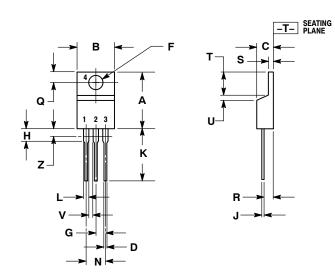
Pulse Test: pulse width ≤ 300 μs, duty cycle ≤ 2%.
Switching characteristics are independent of operating junction temperatures.





#### PACKAGE DIMENSIONS

#### TO-220 CASE 221A-09 ISSUE AH



NOTES:

 DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

2. CONTROLLING DIMENSION: INCH.

 DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.415	9.66	10.53
С	0.160	0.190	4.07	4.83
D	0.025	0.038	0.64	0.96
F	0.142	0.161	3.61	4.09
G	0.095	0.105	2.42	2.66
Н	0.110	0.161	2.80	4.10
J	0.014	0.024	0.36	0.61
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
Ν	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
Т	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
۷	0.045		1.15	
Ζ		0.080		2.04

STYLE 5: PIN 1. GATE 2. DRAIN 3. SOURCE 4. DRAIN

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