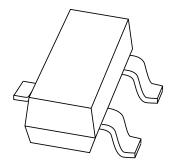
DISCRETE SEMICONDUCTORS

DATA SHEET



BFQ67NPN 8 GHz wideband transistor

Product specification Supersedes data of September 1995



NPN 8 GHz wideband transistor

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FEATURES

- · High power gain
- Low noise figure
- · High transition frequency
- Gold metallization ensures excellent reliability.

APPLICATIONS

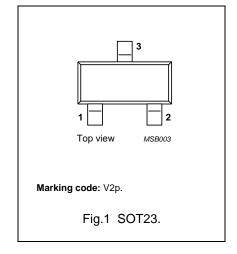
Satellite TV tuners and RF portable communications equipment up to 2 GHz.

DESCRIPTION

Silicon NPN wideband transistor in a plastic SOT23 package.

PINNING

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	_	20	٧
V _{CEO}	collector-emitter voltage	open base	_	_	10	٧
Ic	collector current (DC)		_	_	50	mA
P _{tot}	total power dissipation	T _s ≤ 97 °C; note 1	-	_	300	mW
h _{FE}	DC current gain	I _C = 15 mA; V _{CE} = 5 V	60	100	_	
f _T	transition frequency	I _C = 15 mA; V _{CE} = 8 V	-	8	_	GHz
G _{UM}	maximum unilateral power gain	I _C = 15 mA; V _{CE} = 8 V; f = 1 GHz	-	14	_	dB
F	noise figure	I _C = 5 mA; V _{CE} = 8 V; f = 1 GHz	_	1.3	_	dB

Note

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	-	20	V
V _{CEO}	collector-emitter voltage	open base	_	10	V
V _{EBO}	emitter-base voltage	open collector	_	2.5	V
I _C	collector current (DC)		_	50	mA
P _{tot}	total power dissipation	T _s ≤ 97 °C; note 1	_	300	mW
T _{stg}	storage temperature range		-65	+150	°C
T _i	junction temperature		_	175	°C

Note

1. T_s is the temperature at the soldering point of the collector tab.

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^{1.} T_s is the temperature at the soldering point of the collector tab.

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-s}	thermal resistance from junction to soldering point	note 1	260	K/W

Note

1. T_s is the temperature at the soldering point of the collector lead.

CHARACTERISTICS

 $T_i = 25$ °C unless otherwise specified.

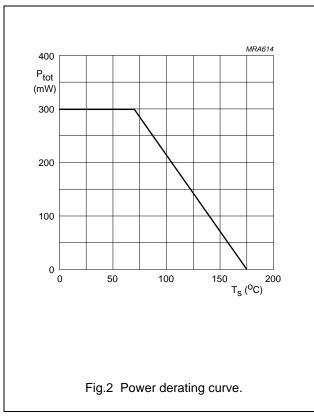
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 5 V	_	_	50	nA
h _{FE}	DC current gain	I _C = 15 mA; V _{CE} = 5 V	60	100	_	
C _c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = 8 \text{ V}$; $f = 1 \text{ MHz}$	_	0.7	_	pF
C _e	emitter capacitance	$I_C = i_c = 0$; $V_{EB} = 0.5 \text{ V}$; $f = 1 \text{ MHz}$	_	1.3	_	pF
C _{re}	feedback capacitance	I _C = 0; V _{CB} = 8 V; f = 1 MHz	_	0.5	_	pF
f _T	transition frequency	I _C = 15 mA; V _{CE} = 8 V	_	8	_	GHz
G _{UM}	maximum unilateral power gain (note 1)	$I_C = 15 \text{ mA}; V_{CE} = 8 \text{ V};$ $T_{amb} = 25 ^{\circ}C; f = 1 \text{ GHz}$	_	14	_	dB
		$I_C = 15 \text{ mA}; V_{CE} = 8 \text{ V}; f = 2 \text{ GHz}$	_	8	_	dB
F	noise figure	$\Gamma_{\rm s} = \Gamma_{\rm opt}$; $I_{\rm C} = 5$ mA; $V_{\rm CE} = 8$ V; $T_{\rm amb} = 25$ °C; $f = 1$ GHz	_	1.3	_	dB
		$\Gamma_{\text{s}} = \Gamma_{\text{opt}}$; $I_{\text{C}} = 15$ mA; $V_{\text{CE}} = 8$ V; $T_{\text{amb}} = 25$ °C; $f = 1$ GHz	_	1.7	_	dB
		$\Gamma_{\text{s}} = \Gamma_{\text{opt}}$; $I_{\text{C}} = 5$ mA; $V_{\text{CE}} = 8$ V; $T_{\text{amb}} = 25$ °C; $f = 2$ GHz	_	2.2	_	dB
		I_C = 5 mA; V_{CE} = 8 V; T_{amb} = 25 °C; f = 2 GHz; Z_s = 60 Ω	_	2.5	_	dB
		$\Gamma_{\text{s}} = \Gamma_{\text{opt}}$; $I_{\text{C}} = 15$ mA; $V_{\text{CE}} = 8$ V; $T_{\text{amb}} = 25$ °C; $f = 2$ GHz	_	2.7	_	dB
		I_C = 15 mA; V_{CE} = 8 V; T_{amb} = 25 °C; f = 2 GHz; Z_s = 60 Ω	_	3	_	dB

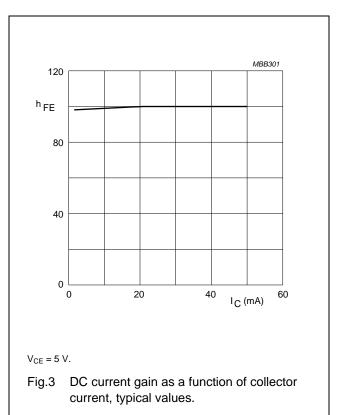
 $\text{Note} \\ 1. \quad G_{UM} \text{ is the maximum unilateral power gain, assuming S_{12} is zero and } \quad G_{UM} = 10 \log \frac{\left|S_{21}\right|^2}{(1-\left|S_{11}\right|^2)(1-\left|S_{22}\right|^2)} \, dB \; .$

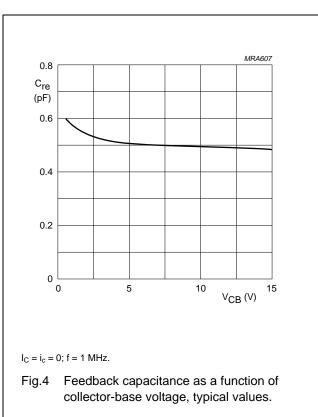
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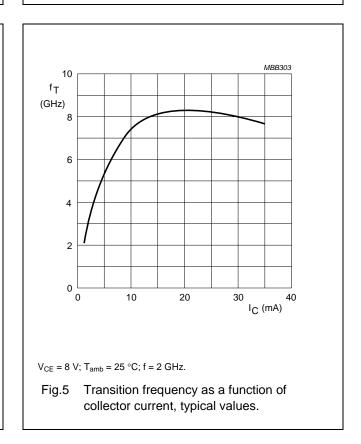
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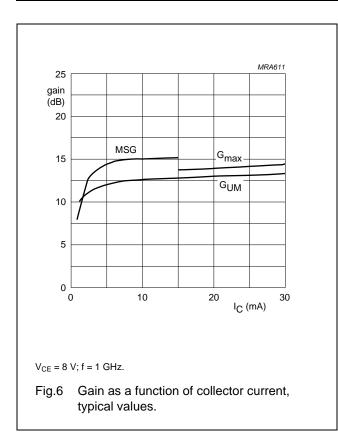


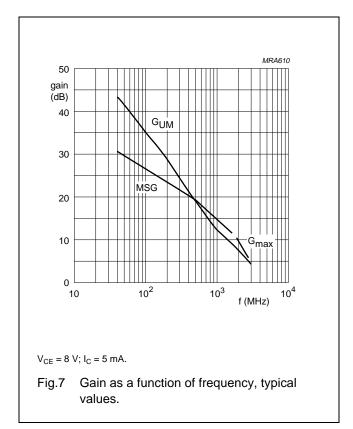


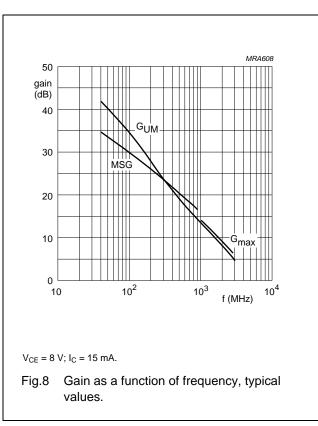
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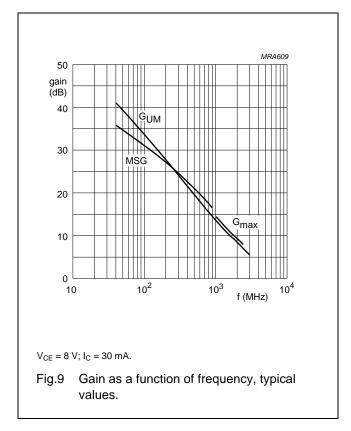
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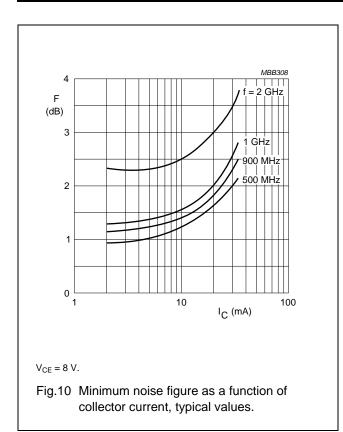


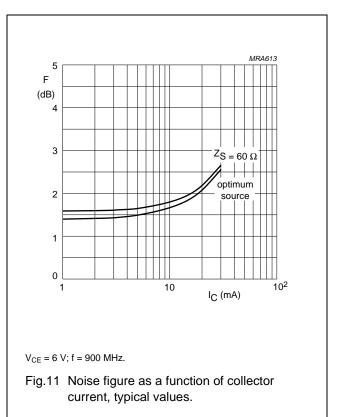


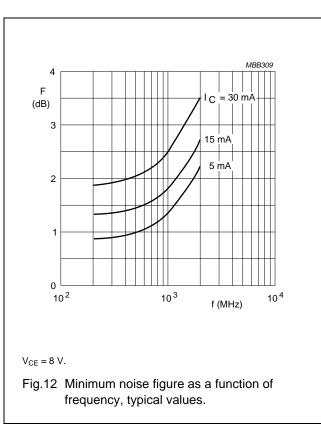


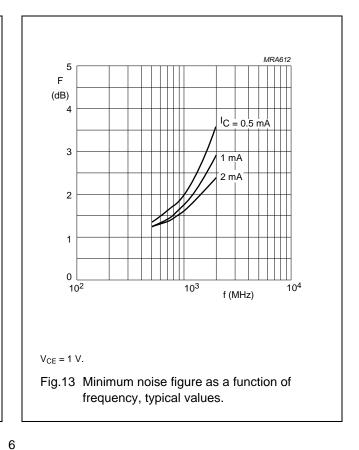
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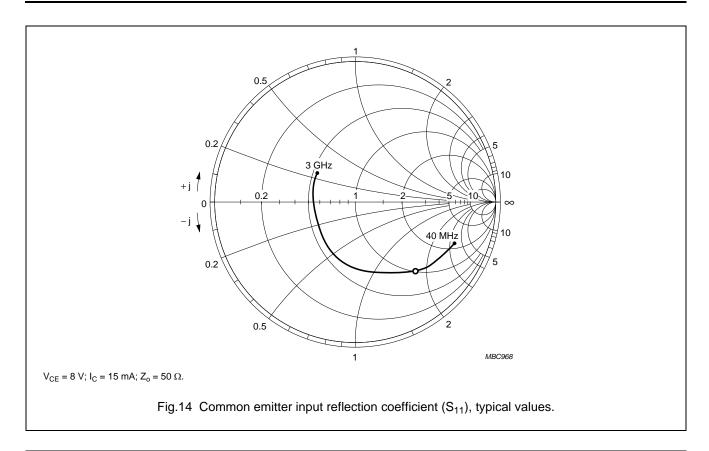


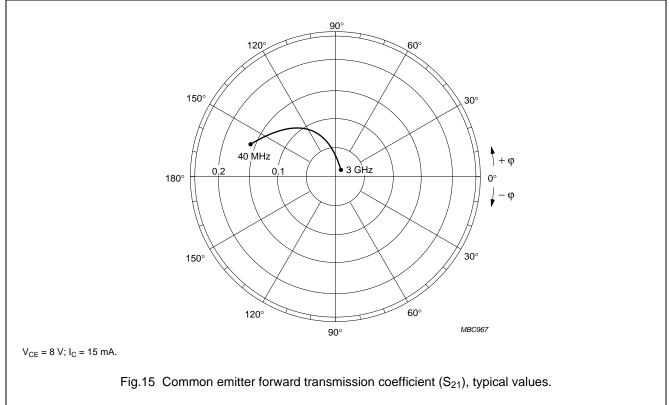




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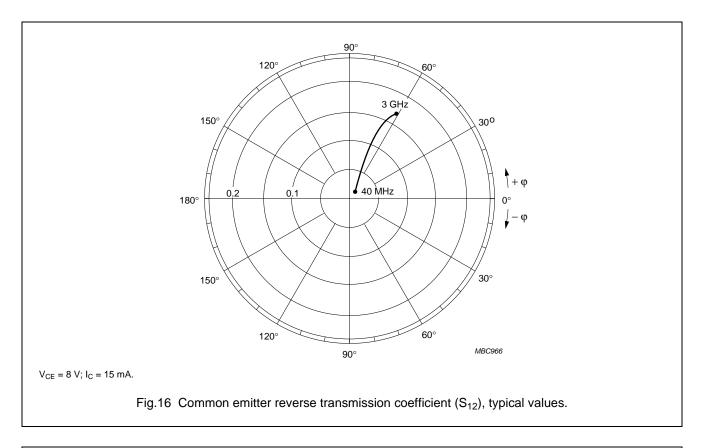


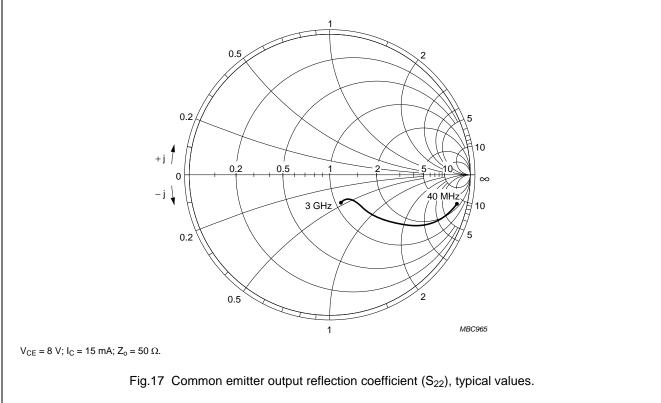


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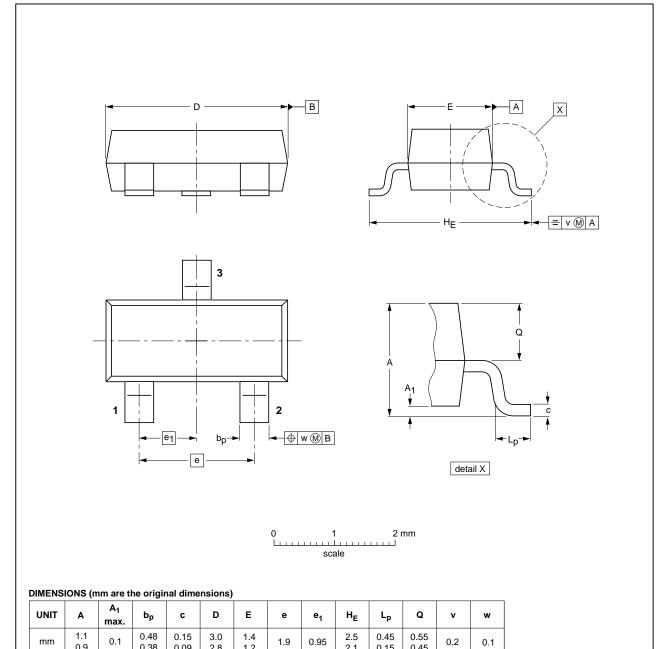
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PACKAGE OUTLINE

Plastic surface-mounted package; 3 leads

SOT23



OUTLINE	OUTLINE REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	1330E DATE
SOT23		TO-236AB				-04-11-04 06-03-16

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0.38

0.9

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DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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Contact information

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