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FAIRCHILD

FGH60N60SMD_F085 600V, 60A Field Stop IGBT

Features

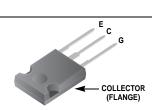
- Maximum Junction Temperature : T_J = 175°C
- Positive Temperaure Co-efficient for easy parallel operating
- High current capability
- Low saturation voltage: V_{CE(sat)} = 1.8V(Typ.) @ I_C = 60A
- High input impedance
- Tightened Parameter Distribution
- RoHS compliant
- Qualified to Automotive Requirements of AEC-Q101

General Description

Using Novel Field Stop IGBT Technology, Fairchild's new series of Field Stop Trench IGBTs offer the optimum performance for Automotive chargers, Solar Inverter, UPS and Digital Power Generator where low conduction and switching losses are essential.

Applications

- Automotive chargers, Converters, High Voltage Auxiliaries
- Solar Inverters, UPS, SMPS, PFC



Absolute Maximum Ratings

Symbol	Description		Ratings	Units
V _{CES}	Collector to Emitter Voltage		600	V
V _{GES}	Gate to Emitter Voltage		± 20	V
I _C	Collector Current	@ T _C = 25°C	120	А
	Collector Current	@ T _C = 100°C	60	A
I _{CM (1)}	Pulsed Collector Current		180	А
I _F	Diode Forward Current	@ T _C = 25°C	60	A
	Diode Forward Current	@ T _C = 100°C	30	A
I _{FM(1)}	Pulsed Diode Maximum Forward Current		180	А
P _D	Maximum Power Dissipation	@ T _C = 25°C	600	W
· D	Maximum Power Dissipation	@ T _C = 100°C	300	W
TJ	Operating Junction Temperature		-55 to +175	°C
T _{stg}	Storage Temperature Range		-55 to +175	°C
Τ _L	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C

Thermal Characteristics

Symbol	Parameter	Ratings	Units	
$R_{\theta JC}(IGBT)_{(2)}$	Thermal Resistance, Junction to Case	0.25	°C/W	
$R_{\theta JC}(Diode)$	Thermal Resistance, Junction to Case	1.1	°C/W	
Symbol	Parameter	Тур.	Units	
$R_{ hetaJA}$	Thermal Resistance, Junction to Ambient (PCB Mount)(2)	45	°C/W	

December 2013

Package Marking and Ordering Information

Device Marking	Device	Package	Packing Type	Qty per Tube	
FGH60N60SMD	FGH60N60SMD_F085	TO-247	Tube	30ea	

For Fairchild's definition of "green" Eco Status, please visit: <u>http://www.fairchildsemi.com/company/green/rohs_green.html</u>.

Electrical Characteristics of the IGBT T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Charac	teristics					
BV _{CES}	Collector to Emitter Breakdown Voltage	$V_{GE} = 0V, I_{C} = 250uA$	600	-	-	V
$\frac{\Delta BV_{CES}}{\Delta T_{J}}$	Temperature Coefficient of Breakdown Voltage	$V_{GE} = 0V, I_{C} = 250uA$	-	0.22	-	V/ºC
I _{CES}	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$	-	-	250	
		I _{CES} at 80%*B _{VCES} , 175°C	-	-	1100	μA
I _{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$	-	-	±400	nA
On Charac	teristics					
V _{GE(th)}	G-E Threshold Voltage	I_{C} = 250uA, V_{CE} = V_{GE}	3.5	4.7	6.0	V
		I _C = 60A, V _{GE} = 15V	-	1.8	2.5	V
V _{CE(sat)}	Collector to Emitter Saturation Voltage	$I_{C} = 60A, V_{GE} = 15V,$ $T_{C} = 175^{o}C$	-	2.14	-	V
Dynamic C	characteristics					
C _{ies}	Input Capacitance		-	2780	3700	pF
C _{oes}	Output Capacitance	V _{CE} = 30V _, V _{GE} = 0V, f = 1MHz	-	260	345	pF
C _{res}	Reverse Transfer Capacitance		-	80	110	pF
Switching	Characteristics					
t _{d(on)}	Turn-On Delay Time		-	22	29	ns
t _r	Rise Time		-	46	60	ns
t _{d(off)}	Turn-Off Delay Time	$V_{\rm CC} = 400 V, I_{\rm C} = 60 A,$	-	116	151	ns
t _f	Fall Time	$R_{G} = 3\Omega, V_{GF} = 15V,$	-	14	18	ns
Eon	Turn-On Switching Loss	Inductive Load, $T_C = 25^{\circ}C$	-	1.59	2.23	mJ
E _{off}	Turn-Off Switching Loss		-	0.39	0.55	mJ
E _{ts}	Total Switching Loss		-	1.98	2.78	mJ
t _{d(on)}	Turn-On Delay Time		-	22	28	ns
t _r	Rise Time		-	44	58	ns
t _{d(off)}	Turn-Off Delay Time	$V_{CC} = 400V, I_C = 60A,$	-	124	161	ns
t _f	Fall Time	$R_{G} = 3\Omega, V_{GE} = 15V,$	-	15	20	ns
Eon	Turn-On Switching Loss	Inductive Load, $T_C = 175^{\circ}C$	-	2.41	3.13	mJ
E _{off}	Turn-Off Switching Loss		-	1.08	1.42	mJ
E _{ts}	Total Switching Loss]	-	3.49	4.55	mJ

Notes:

1:Repetitive rating: Pulse width limited by max junction temperature.

2:Rthjc for TO-247 : according to Mil standard 883-1012 test method. Rthja for TO-247 : according to JESD51-2, test method environmental condition and JESD51-10, test boards for through hole perimeter leaded package thermal measurements. JESD51-3 : Low Effective Thermal Conductivity Test Board for Leaded Surface Mount Package.

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Electrical Characteristics of the IGBT (Continued)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max	Units
Qg	Total Gate Charge		-	187	280	nC
Q _{ge}	Gate to Emitter Charge	V _{CE} = 400V, I _C = 60A, V _{GE} = 15V	-	20	29	nC
Q _{gc}	Gate to Collector Charge	VGE - 10V	-	92	138	nC

Electrical Characteristics of the Diode $T_{C} = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Тур.	Max	Units
V _{FM}	V _{EM} Diode Forward Voltage	I _E = 30A	$T_{\rm C} = 25^{\rm o}{\rm C}$	-	2.1	2.7	V
- FIM	2.040 Formard Formage	.F CON	T _C = 175°C	-	1.48	-	
t _{rr}	Diode Reverse Recovery Time	I _F =30A, dI _F /dt = 200A/μs	$T_{\rm C} = 25^{\rm o}{\rm C}$	-	33	42	ns
۹r			$T_{\rm C} = 175^{\rm o}{\rm C}$	-	115	-	110
Q _{rr}	Diode Reverse Recovery Charge		$T_{\rm C} = 25^{\rm o}{\rm C}$	-	53	69	nC
α _{II}	Didde Neveree Needvory enarge		$T_{C} = 175^{\circ}C$	-	606	-	110

Typical Performance Characteristics Figure 1. Typical Output Characteristics 180 V_{GE}=20V -15V 12V 10V Collector Current, I_c [A] 120 60 8V T_C = 25°C 0 2 6 8 0 4 10 Collector-Emitter Voltage, V_{CE} [V] **Figure 3. Typical Saturation Voltage** Characteristics 180 Collector Current, I_c [A] 120 60 Common Emitter V_{GE} = 15V T_C = 25°C ___ T_C = 175^oC ···-0 ∟ 0 1 2 3 4 Collector-Emitter Voltage, V_{CE} [V] 5 Figure 5. Saturation Voltage vs. Case **Temperature at Variant Current Level** 4 Common Emitter $V_{GE} = 15V$ Collector-Emitter Voltage, V_{CE} [V] 120A 3 60Å 2 Ic = 30A 1 25 50 100 125 150 75 175 Collector-EmitterCase Temperature, T_C [°C]

Figure 2. Typical Output Characteristics

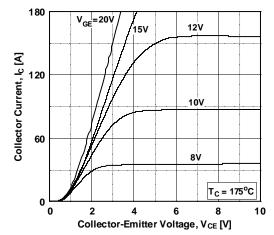
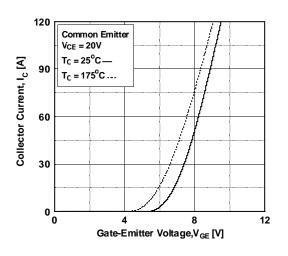
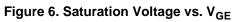
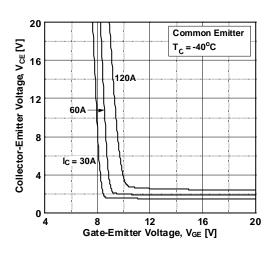


Figure 4. Transfer Characteristics

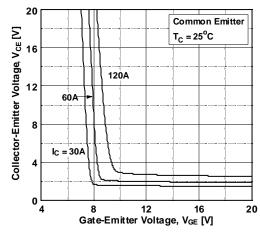






Typical Performance Characteristics







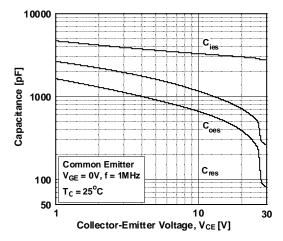


Figure 11. SOA Characteristics

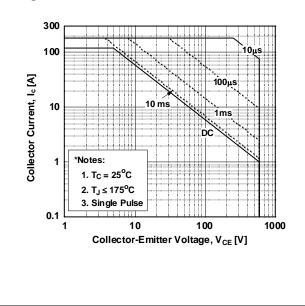


Figure 8. Saturation Voltage vs. V_{GE}

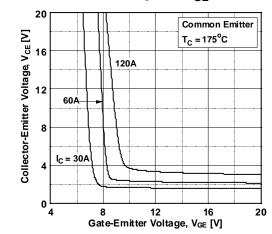


Figure 10. Gate charge Characteristics

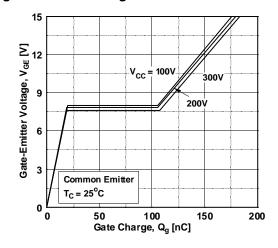
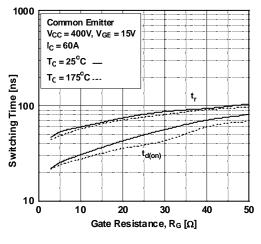
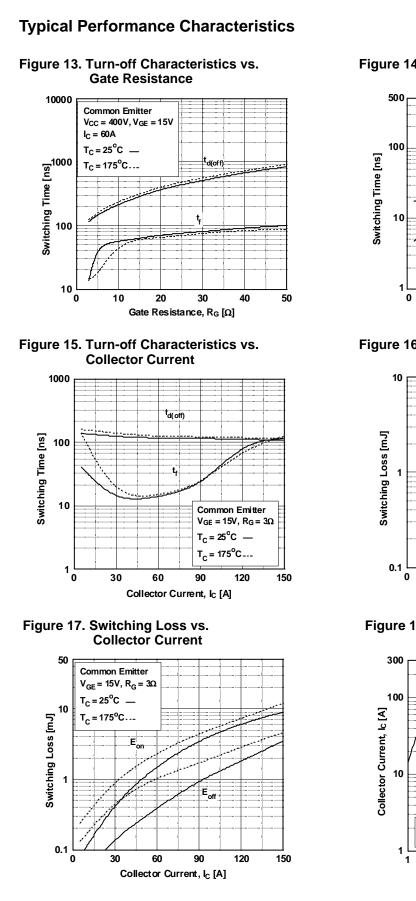
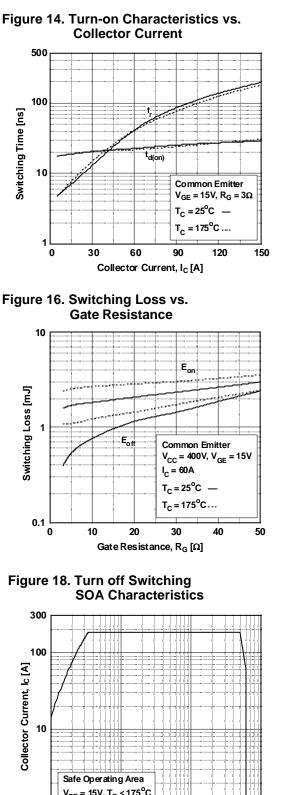
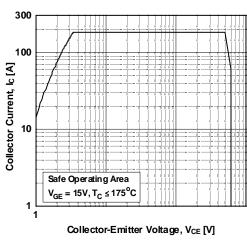


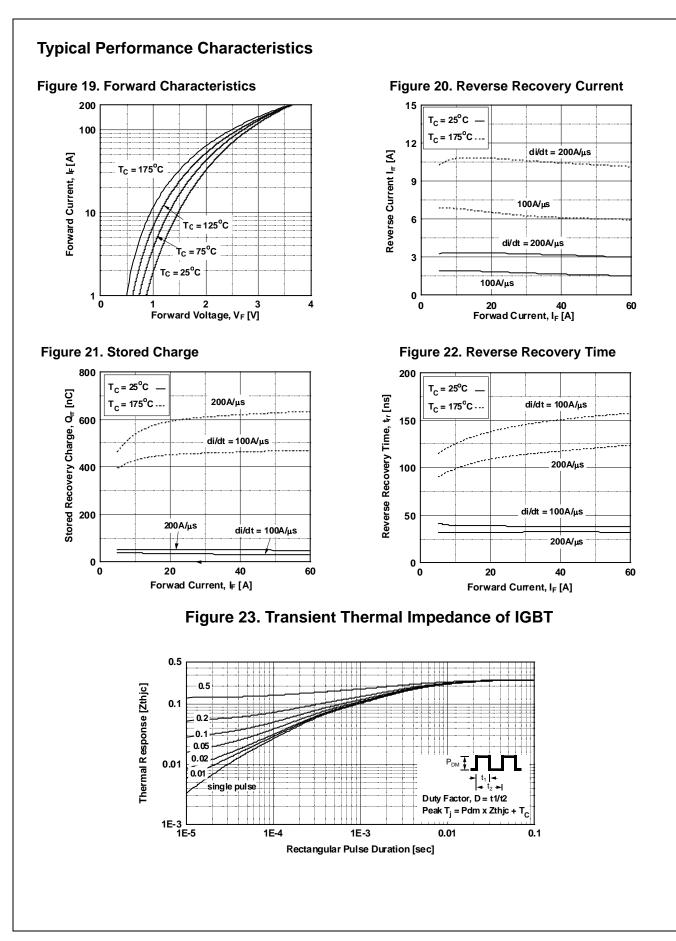
Figure 12. Turn-on Characteristics vs. Gate Resistance

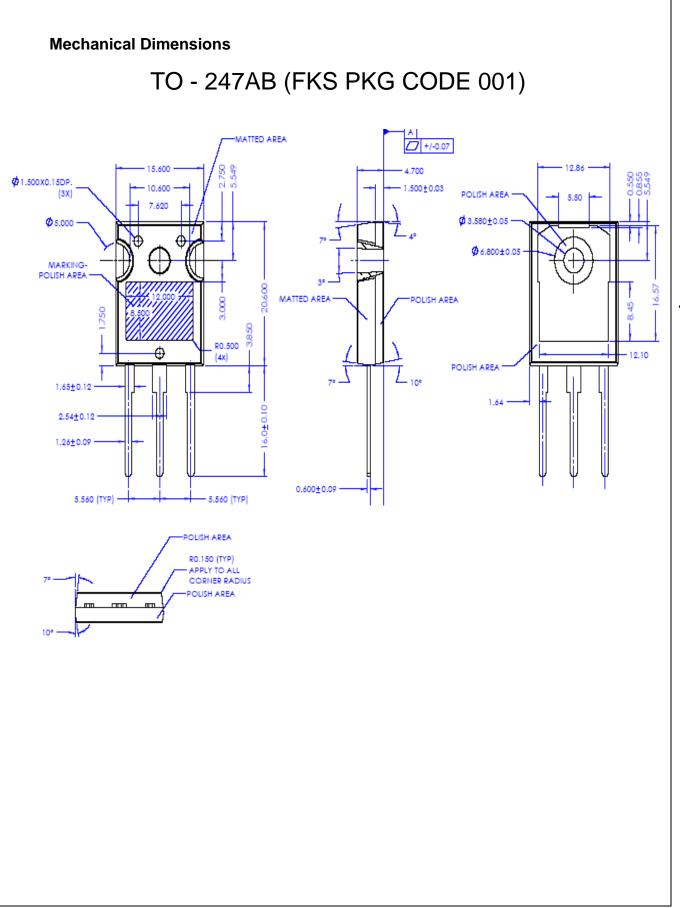














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