2.5V Drive Pch MOS FET

RTQ030P02

●Structure

Silicon P-channel MOSFET

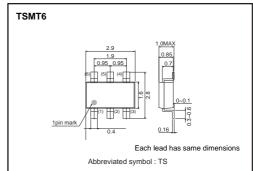
Features

- 1) Low On-resistance.(110m Ω at 2.5V)
- 2) High Power Package.
- 3) High speed switching.
- 4) Low voltage drive.(2.5V)

Applications

DC-DC converter

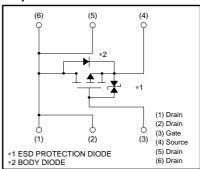
●External dimensions (Unit : mm)



Packaging specifications

	Package	Taping
Туре	Code	TR
	Basic ordering unit (pieces)	3000
RTQ030P02	0	

●Equivalent circuit



● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit			
Drain-source voltage		Voss	-20	V			
Gate-source voltage		Vgss	±12	V			
Drain current	Continuous	ΙD	±3	A			
	Pulsed	IDP *1	±12	А			
Source current (Body diode)	Continuous	ls	-1	A			
	Pulsed	Isp *1	-4	A			
Total power dissipation		P _D *2	1.25	W			
Channel temperature		Tch	150	°C			
Range of Storage temperature		Tstg	-55 to +150	°C			

^{*1} Pw≤10µs, Duty cycle≤1% *2 Mounted on a ceramic board

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a) *	100	°C/W
* Mounted on a ceramic board.			

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	Igss	-	_	±10	μΑ	Vgs=±12V, Vds=0V	
Drain-source breakdown voltage	V _{(BR)DSS}	-20	_	_	V	I _D =-1mA, V _G S=0V	
Zero gate voltage drain current	IDSS	_	-	-1	μΑ	V _{DS} =-20V, V _{GS} =0V	
Gate threshold voltage	VGS(th)	-0.7	-	-2.0	V	VDS=-10V, ID=-1mA	
Static drain-source on-state resistance		-	60	80	mΩ	In=-3A, Vgs=-4.5V	
	RDS(on)	_	65	90	mΩ	ID=-3A, VGS=-4V	
		-	110	150	mΩ	In=-1.5A, Vgs=-2.5V	
Foward transfer admittance	Yfs *	2.0	_	_	S	VDS=-10V, ID=-1.5A	
Input capacitance	Ciss	_	800	_	pF	V _{DS} =-10V,V _{GS} =0V f=1MHz	
Output capacitance	Coss	-	150	_	pF		
Reverse transfer capacitance	Crss	_	100	_	pF		
Turn-on delay time	td(on) *	_	15	_	ns	ID=-1.5A VDD≒-15V VGS=-4.5V RL=10Ω RG=10Ω	
Rise time	tr *	_	27	_	ns		
Turn-off delay time	td(off) *	_	50	_	ns		
Fall time	t _f *	_	20	-	ns		
Total gate charge	Qg	-	9.0	-	nC	V _{DD} ≒-15V V _{GS} =-4.5V I _D =-3A	
Gate-source charge	Qgs	-	1.6	_	nC		
Gate-drain charge	Qgd	ı	4.6	_	nC		

^{*}PULSED

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp	-	-	-1.2	V	Is=-1A, Vgs=0V

Electrical characteristic curves 1000 Static Drain–Source On–State Resistance Res(on)[$m\Omega$] Static Drain–Source On–State Resistance Ros(on)[$m\Omega$] Drain Current: -lp (A) 0.01 0.001 Gate-Source Voltage : -Vgs[V] $Drain\ Current: -I_D[A]$ Drain Current : -Ip[A] Fig.2 Static Drain-Source On-State Fig.1 Typical Transfer Characteristics Fig.3 Static Drain-Source On-State Resistance vs. Drain Current Resistance vs.Drain Current Static Drain–Source On–State Resistance Resistance Resion[mΩ] Static Drain–Source On–State Resistance Res(on)[m Ω] :-lbr[A] Reverse Drain Current: 0.0 Drain Current : -Ip[A] Source-Drain Voltage : -Vsp[V] Drain Current : -Ip[A] Fig.4 Static Drain-Source On-State Fig.5 Static Drain-Source On-State Fig.6 Reverse Drain Current Resistance vs.Drain-Current Resistance vs. Drain-Current vs. Source-Drain Voltage 10000 1000 Ta=25°C VDD=-15\ Ta=25°C VDD=-15\ ID=-3.0V RG=10Ω Gate-Source Voltage: -Vos [V] Switching Time : t [ns] Capacitance : C [pF] 1000 100 10 L 0.0 1 L 10 Drain-Source Voltage : -Vps[V] Drain Current : -ID[A] Total Gate Charge : Qg[nC] Fig.7 Typical Capactitance Fig.8 Switching Characteristics vs.Drain-Source Voltage Fig.9 Dynamic Input Characteristics

Measurement circuits

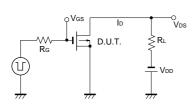


Fig.10 Switching Time Measurement Circuit

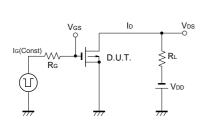


Fig.12 Gate Charge Measurement Circuit

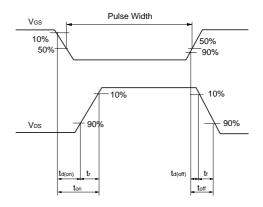


Fig.11 Switching Waveforms

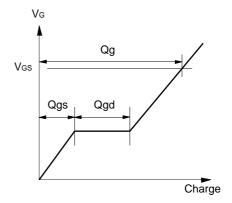


Fig.13 Gate Charge Waveforms

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