

8

7

6

5

4

3

2

1

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RELAY CONTROL CHART

CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR

JUMPER TABLE		
JP#	ON	OFF
1		
2		
3		
4		
5		

* SEE ASSEMBLY INSTRUCTIONS

REVISIONS

REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	XX/XX	X.X.
B	AS PER ECR#045386	03MAR13	A. MAHAFFEY

TEMPLATE ENGINEER
G. CELEDONIO

DATE

HARDWARE SERVICES
R. MACDONALD

HARDWARE SYSTEMS
J. KEANE

TEST ENGINEER

COMPONENT ENGINEER
G. CELEDONIO

TEST PROCESS

HARDWARE RELEASE

DESIGNER
LABRANCHE

PTD ENGINEER
M. DUFF

CHECKER

SCHEMATIC

EVAL BOARD
MULTIPLE FB BAND-PASS SINGLE Z
<PRODUCT_1>
<DRAWING_TITLE1>
<DRAWING_TITLE2>
<DRAWING_TITLE3>
<DRAWING_TITLE4>
<DRAWING_TITLE5>

MASTER PROJECT TEMPLATE
<DRAWING_TITLE6>

TESTER TEMPLATE
-

DRAWING NO.
02-034900

REV.
B

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES

DECIMALS
X.XX +-.0.010
X.XXX +-.0.005

FRACTIONS
+-.1/32

ANGLES
+-.2

SIZE
D

SCALE
<SCALE>-

CODE ID NO.

SHEET 1 OF 2

8

7

6

5

4

3

2

1

A

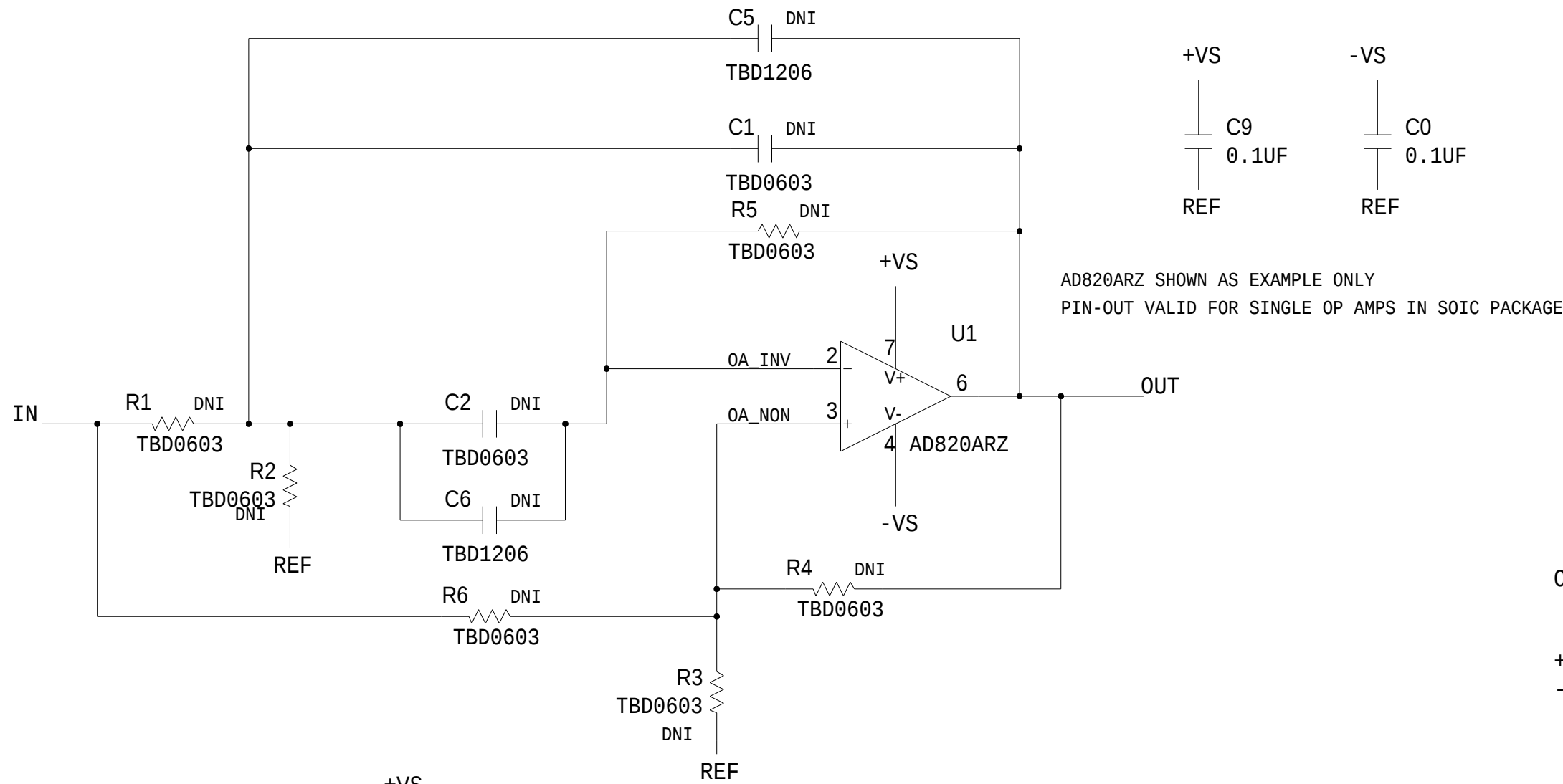
B

C

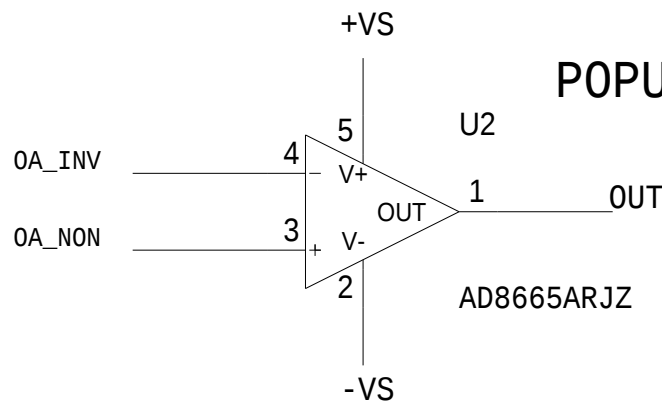
D

MULTIPLE FEEDBACK BANDPASS FILTER STAGE FOR SINGLE OP AMPS

TO ALLOW USE OF LARGER VALUE COG/NPO CAPACITORS,
BOARD INCLUDES 1206 PACKAGE OPTION

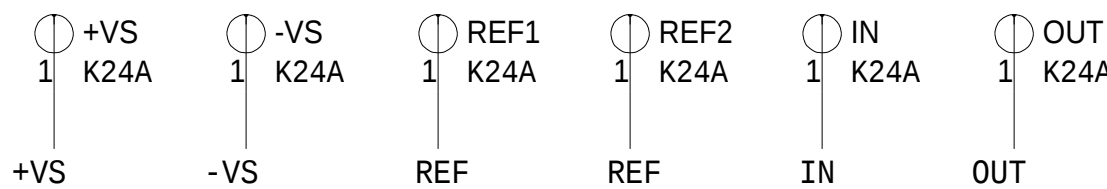


AD820ARZ SHOWN AS EXAMPLE ONLY
PIN-OUT VALID FOR SINGLE OP AMPS IN SOIC PACKAGE



POPULATE EITHER U1 OR U2, NOT BOTH

AD8665ARJZ SHOWN AS EXAMPLE ONLY
PIN-OUT VALID FOR SINGLE OP AMPS IN SOT-23 PACKAGE



	SCHEMATIC			
	EVAL BOARD MULTIPLE FB BAND-PASS SINGLE Z <PRODUCT_1>			
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02-034900	REV B	
	PTD ENGINEER M DUFF	SIZE D	SCALE <SCALE>	SHEET 2 OF 2