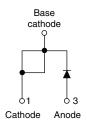


Vishay High Power Products

High Performance Schottky Generation 5.0, 10 A





PRODUCT SUMMARY			
I _{F(AV)}	10 A		
V_R	100 V		

FEATURES

- 175 °C high performance Schottky diode
- Very low forward voltage drop
- Extremely low reverse leakage
- Optimized V_F vs. I_R trade off for high efficiency
- Increased ruggedness for reverse avalanche capability
- RBSOA available
- Negligible switching losses
- · Submicron trench technology
- Full lead (Pb)-free and RoHS compliant devices
- Designed and qualified for industrial level

APPLICATIONS

- High efficiency SMPS
- · Automotive
- · High frequency switching
- · Output rectification
- · Reverse battery protection
- · Freewheeling
- · Dc-to-dc systems
- · Increased power density systems

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	10	А	
V _{RRM}		100	V	
V _F	10 Apk, T _J = 125 °C (typical)	0.62]	
T _J	Range	- 55 to 175	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MBR10T100	UNITS
Maximum DC reverse voltage	V_{R}	T _J = 25 °C	100	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _C = 159 °C, rectangular waveform		10	Α
Maximum peak one cycle non-repetitive surge current	l=o	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	850	A
	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	200	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 3 \text{A}, L = 12 \text{mH}$		54	mJ
Repetitive avalanche current	I _{AR}	Limited by frequency of operation and time pulse duration so that $T_J < T_J$ max. I_{AS} at T_J max. as a function of time pulse See fig. 8		I _{AS} at T _J max.	А

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MBR10T100

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Forward voltage drop per leg	V _{FM} ⁽¹⁾	10 A	T _J = 25 °C	-	0.79	· V
		20 A		-	0.88	
		10 A	T _J = 125 °C	-	0.68	
		20 A		-	0.8	
Reverse leakage current per leg I _{RM}	ı (1)	T _J = 25 °C	V _B = Rated V _B	-	100	μΑ
	'RM \''	T _J = 125 °C	V _R = nateu V _R		4	mA
Junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		400	-	pF
Series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	-	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R		-	10 000	V/μs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature rang	e	T _J , T _{Stg}		- 55 to 175	°C	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	2	2011	
Typical thermal resistance case to heatsink	Э,	R _{thCS}	Mounting surface, smooth and greased	0.5	°C/W	
A considerate mainta				2	g	
Approximate weight			0.07	oz.		
Mounting torque ——	minimum			6 (5)	kgf ⋅ cm	
	maximum			12 (10)	(lbf \cdot in)	
Case style				TO-2	TO-220AC	
Marking device				MBR10T100		

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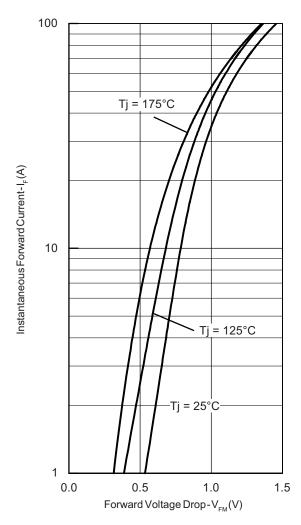


Fig. 1 - Maximum Forward Voltage Drop Characteristics

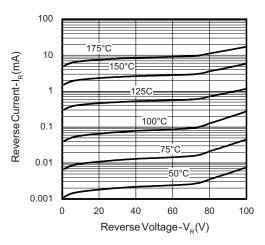


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

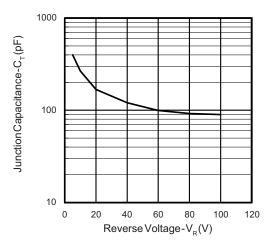


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

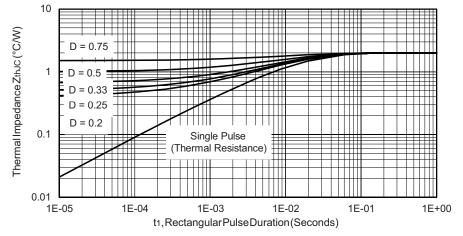


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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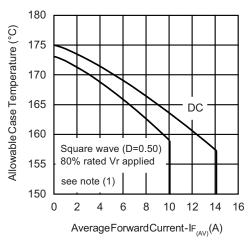


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

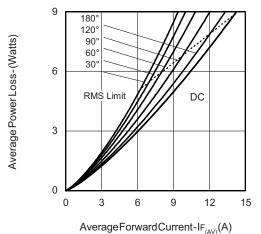
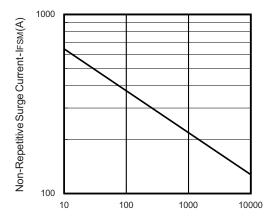


Fig. 6 - Forward Power Loss Characteristics



SquareWavePulseDuration-t, (microsec)

Fig. 7 - Maximum Non-Repetitive Surge Current

Note

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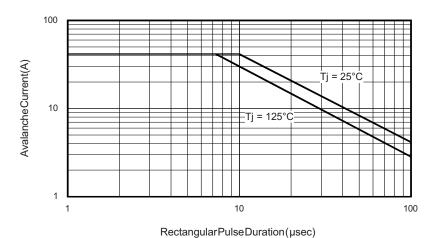


Fig. 8 - Reverse Bias Safe Operating Area (Avalanche Current vs. Rectangular Pulse Duration)

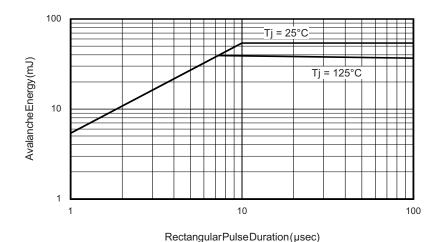


Fig. 9 - Reverse Bias Safe Operating Area (Avalanche Energy vs. Rectangular Pulse Duration)

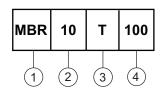
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ORDERING INFORMATION TABLE

Device code



- 1 MBR series
- Current rating (10 = 10 A)
- 3 T = Trench
- 4 Voltage rating (100 = 100 V)

Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95221				
Part marking information	http://www.vishay.com/doc?95224			



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