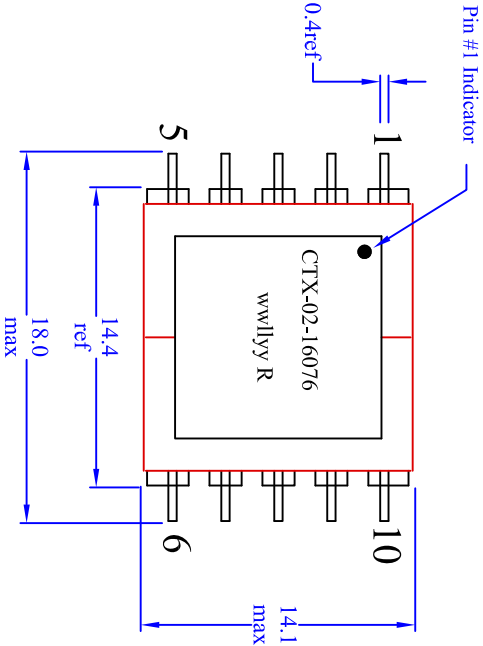
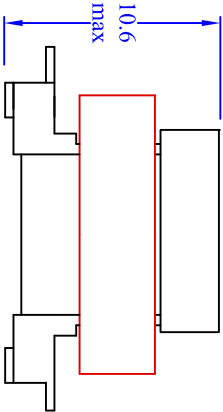


TOP VIEW



FRONT VIEW

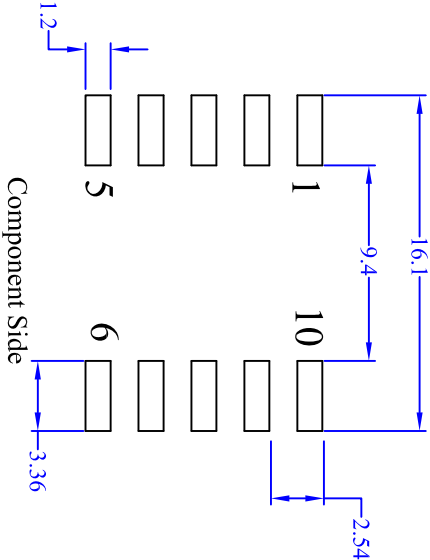


Notes:

- 1). All dimensions are in mm unless otherwise specified.
- 2). Tolerances are +/- 0.25 millimeters unless stated otherwise.
- 3). All soldering surfaces must be coplanar within 0.1524 millimeters.
- 4). Core wrapped with 2 layers of epoxy tape.
- 5). wwllyy= Date Code, R= revision level
- 6). PCB tolerances are +/-0.1 millimeters unless stated otherwise.
- 6). RoHS compliant.

TEMPLATE NAME: SG7.dot REV X5

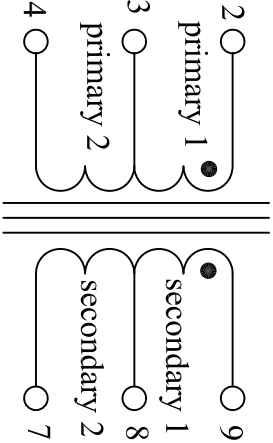
RECOMMENDED PCB LAYOUT



ELECTRICAL CHARACTERISTICS

OCL @ 10KHz, 0.10Vrms, 0.0A_{dc}; pins (2 - 3). 0.825 - 1.38mH.
DCR @ 20 degrees C., pins (2 - 3). 0.24 Ohm maximum.
DCR @ 20 degrees C., pins (3 - 4).: 0.24 Ohms maximum.
DCR @ 20 degrees C., pins (9 - 8). 1.05 Ohm maximum.
DCR @ 20 degrees C., pins (8 - 7): 1.05 Ohms maximum.
Turns ratio: pins (2 - 3) : (3 - 4) : (9 - 8) : (8 - 7). 1.0:1.0:1.4:1.4
HiPot @ 500Vdc for 1 second; pins 3 to 8 and windings to core:

SCHEMATIC



COOPER ELECTRONIC TECHNOLOGIES

1225 Broken Sound Parkway, Suite F, Boca Raton, FL 33487

Transformer, push-pull, continuous

10.8 - 13.2Vdc(input) : 12Vdc @ 0.60A

100kHz, 1.2mH @ 0.80A_{dc}(peak)

EE12.6, 10 Pin J-Lead, Horizontal, SMT

Size Drawing Number: CTX02-16076

A Revision Level: A Sheet 3 of 5